

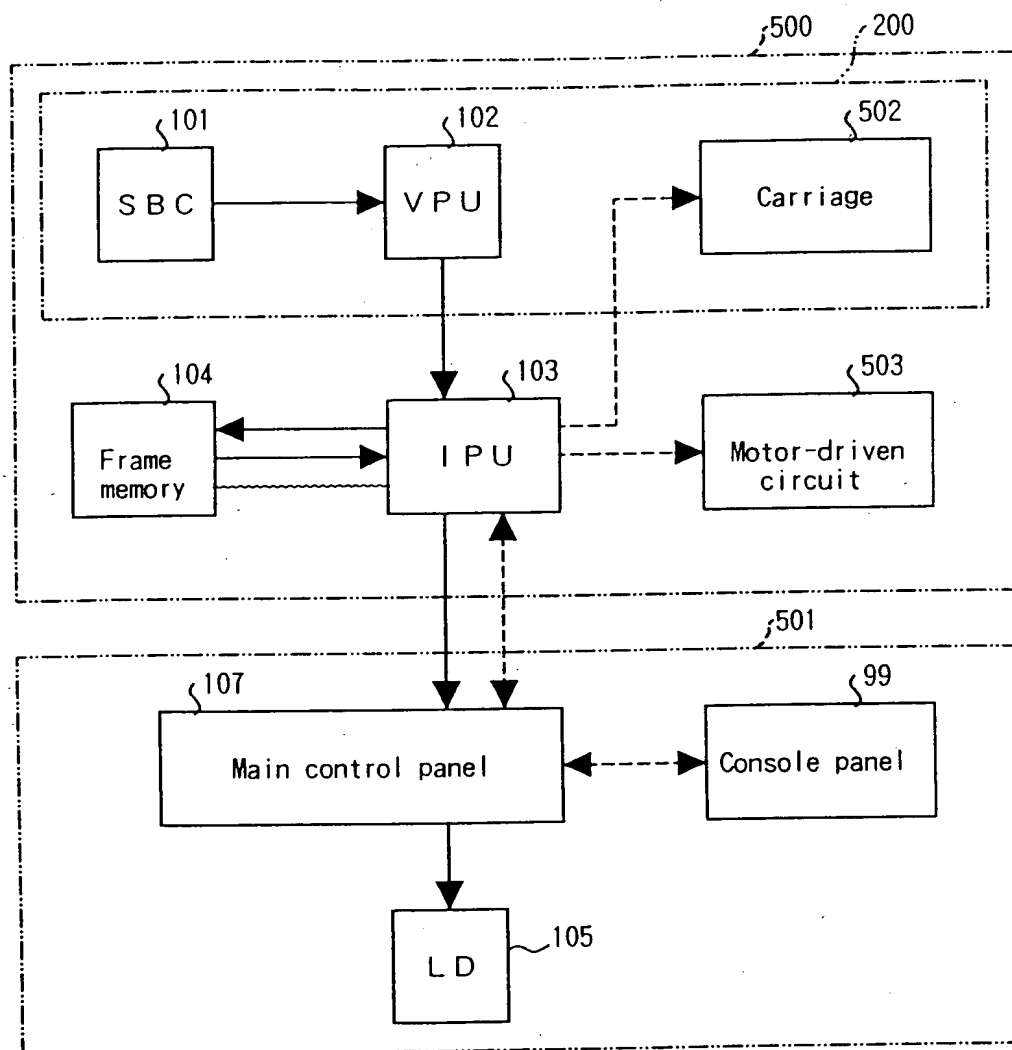
FIG. 1

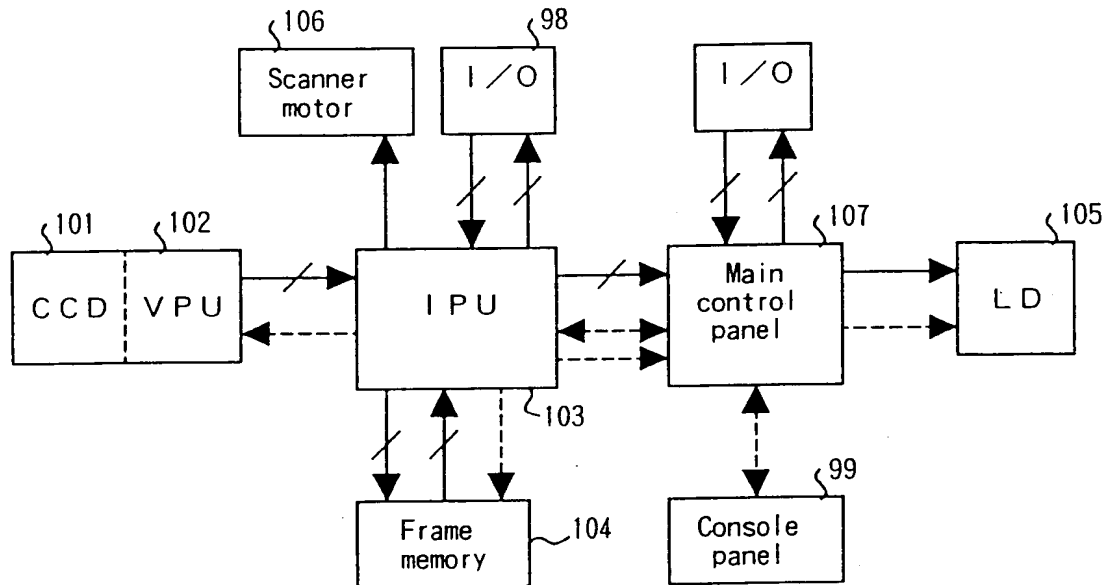
FIG.2

FIG. 3

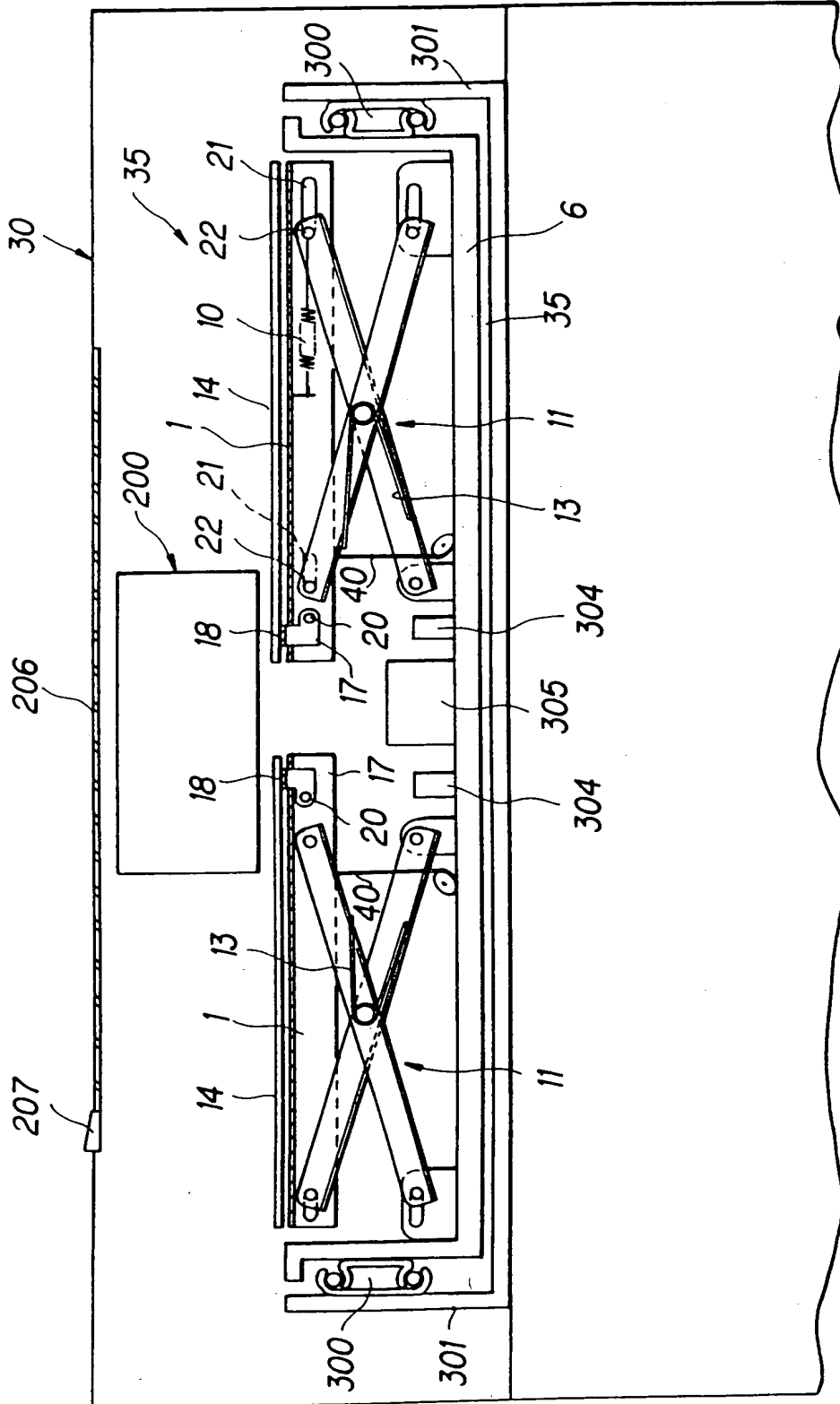


FIG. 4

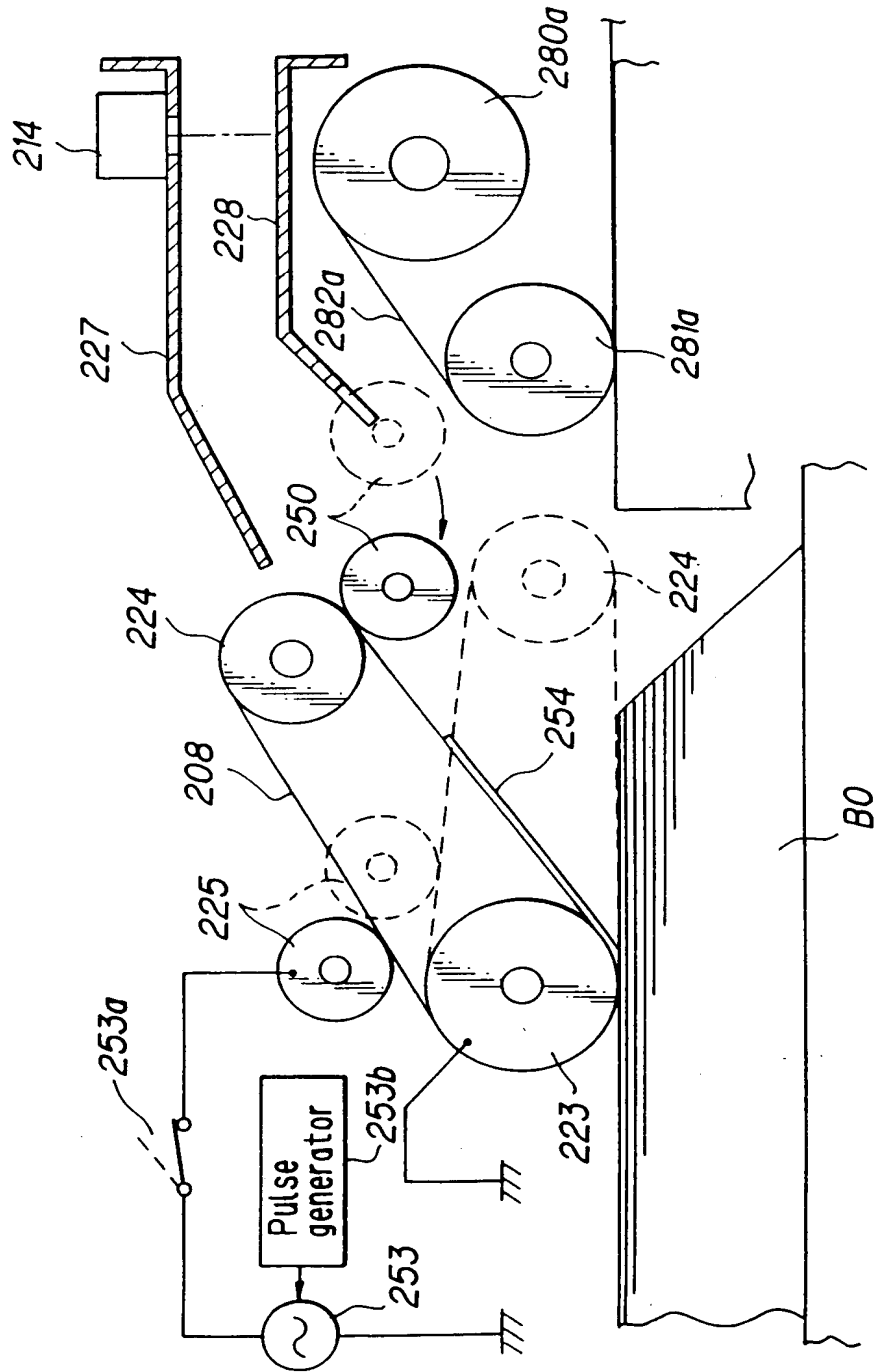


FIG. 5

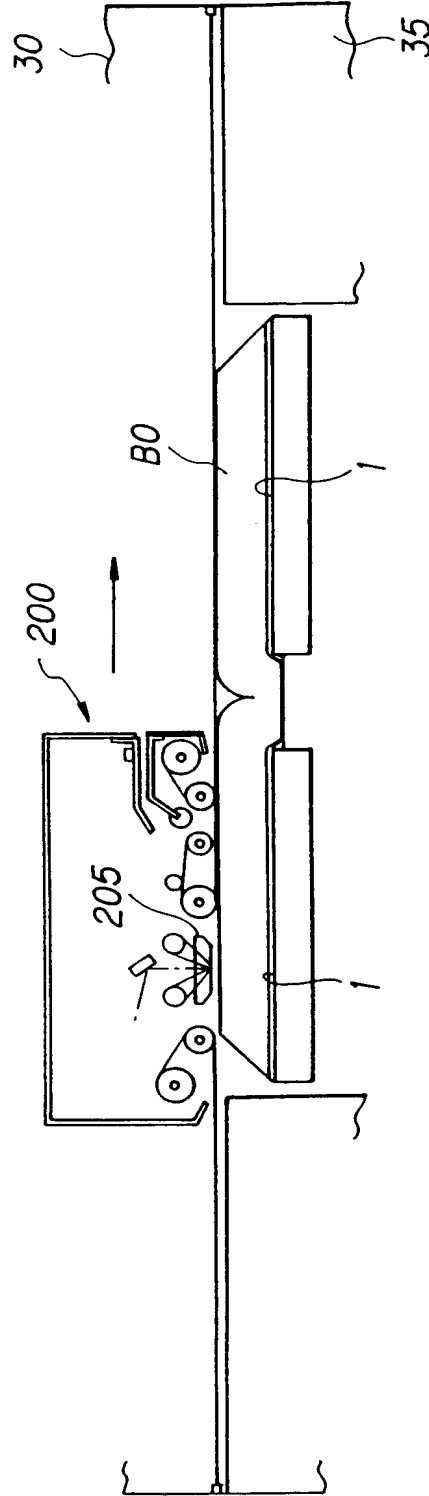


FIG. 6

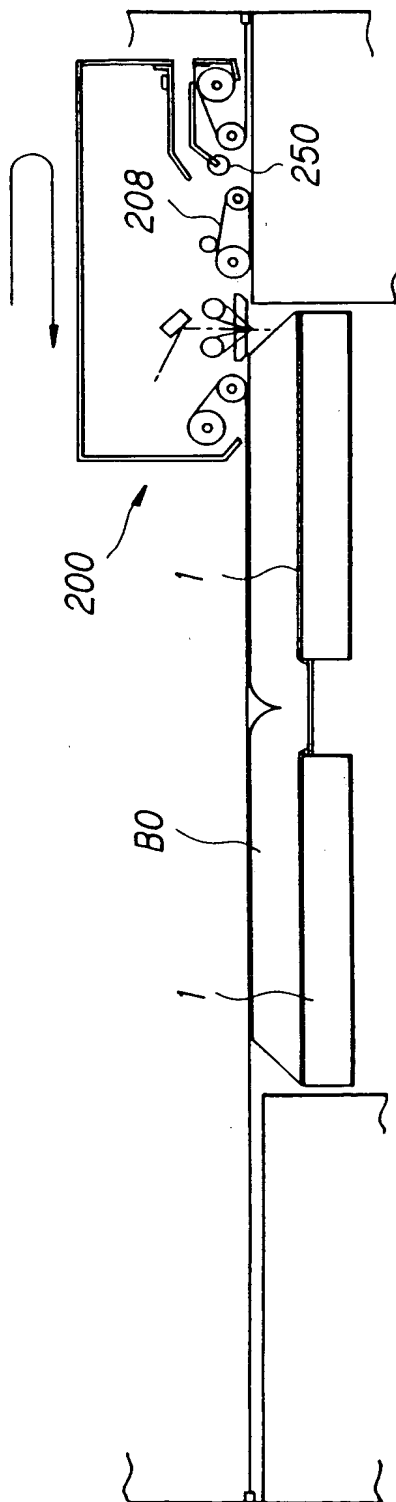


FIG. 7

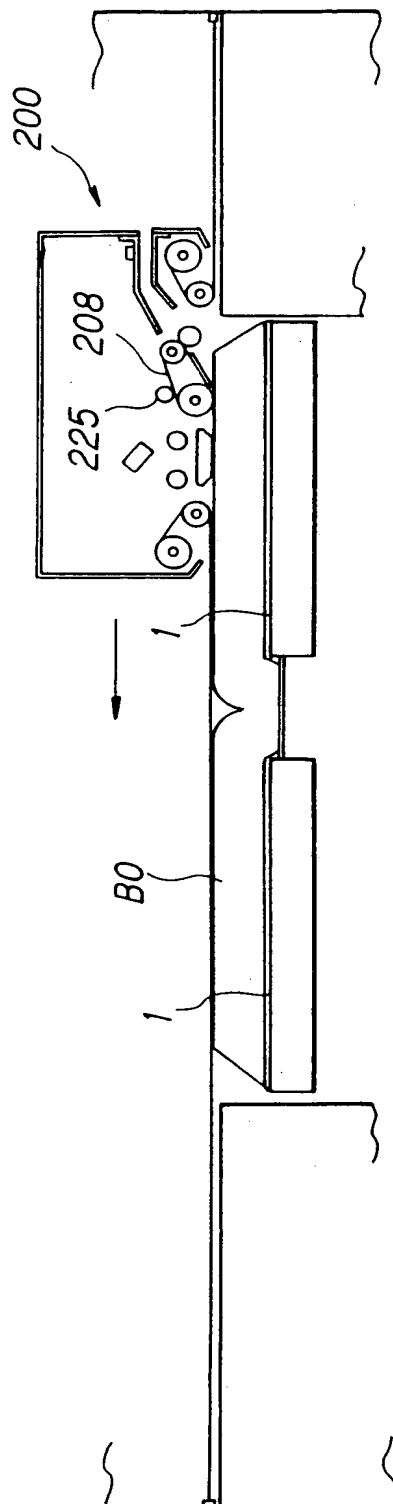


FIG. 8

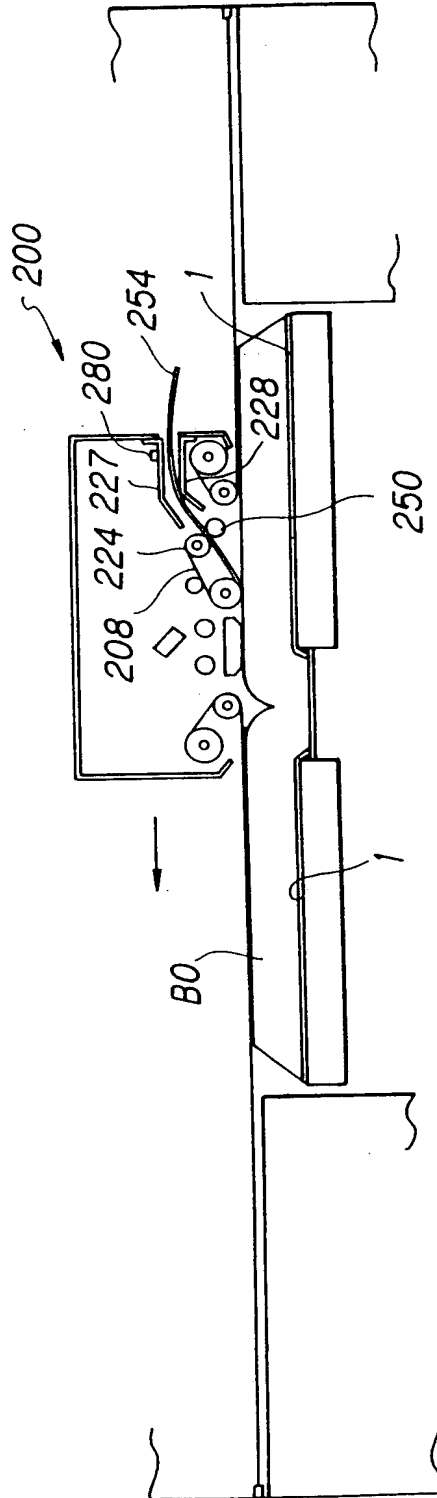


FIG. 9

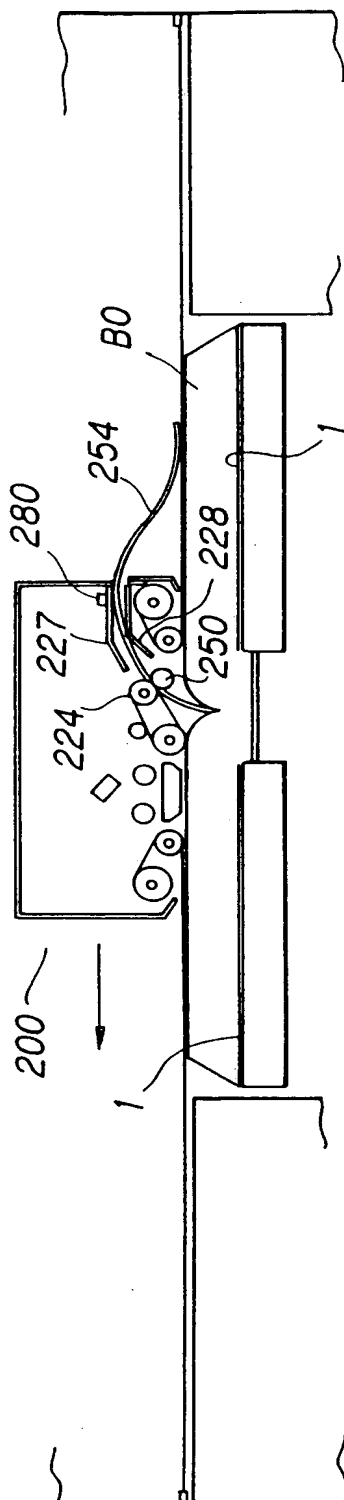


FIG. 10

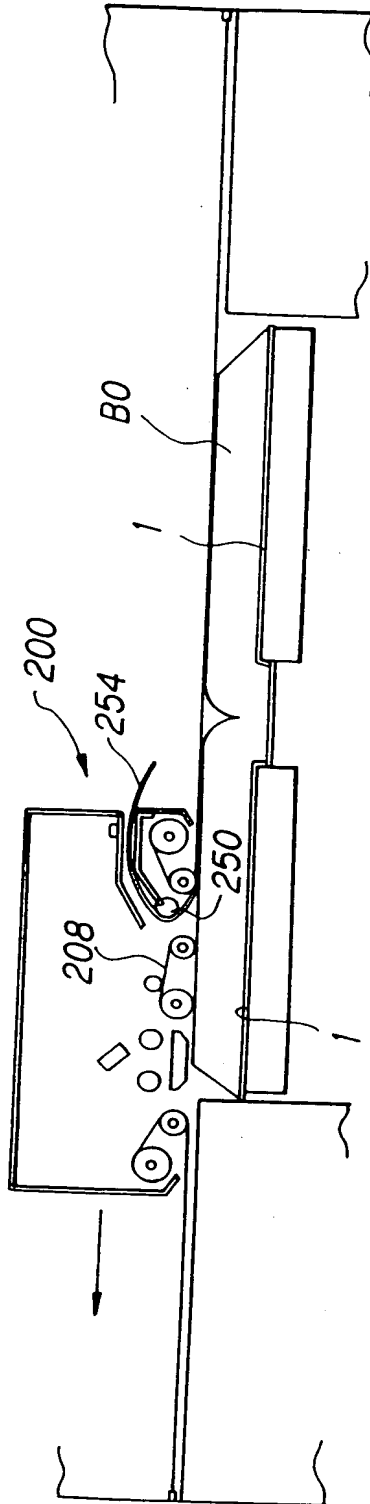
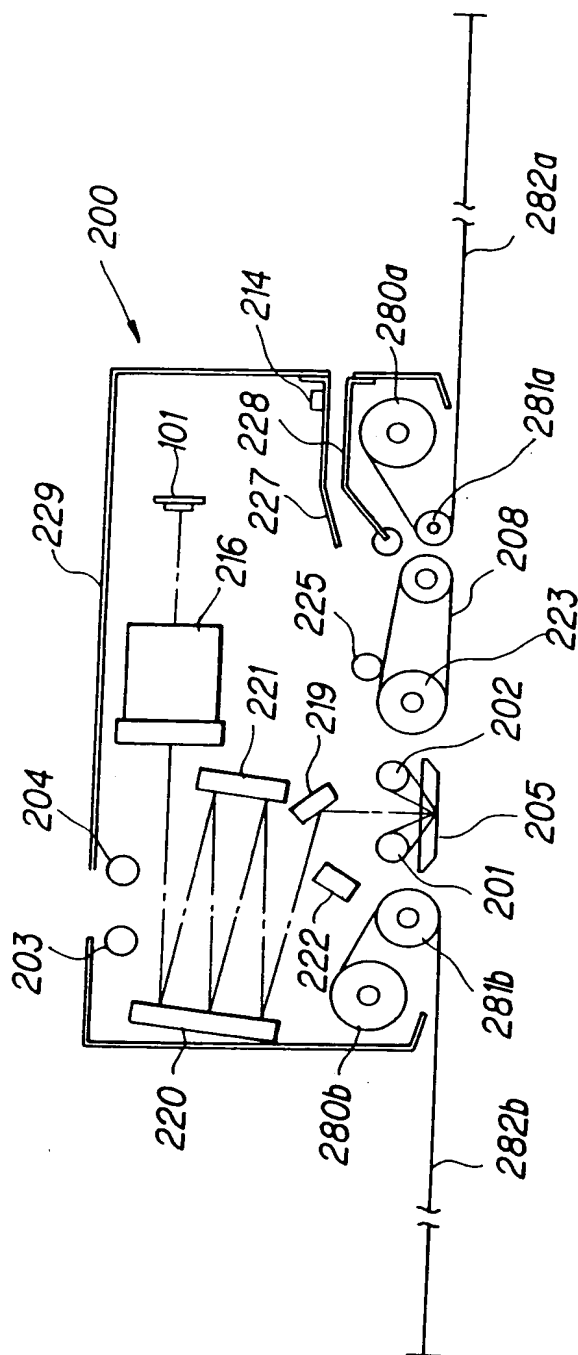


FIG. 11



0956911 092101 101250 1695660

12/50

OBLON, SPIVAK, ET AL
DOCKET #: 209560US-2-DIV
INV: Hiroshi TAKAHASHI
SHEET 12 OF 50

FIG. 12

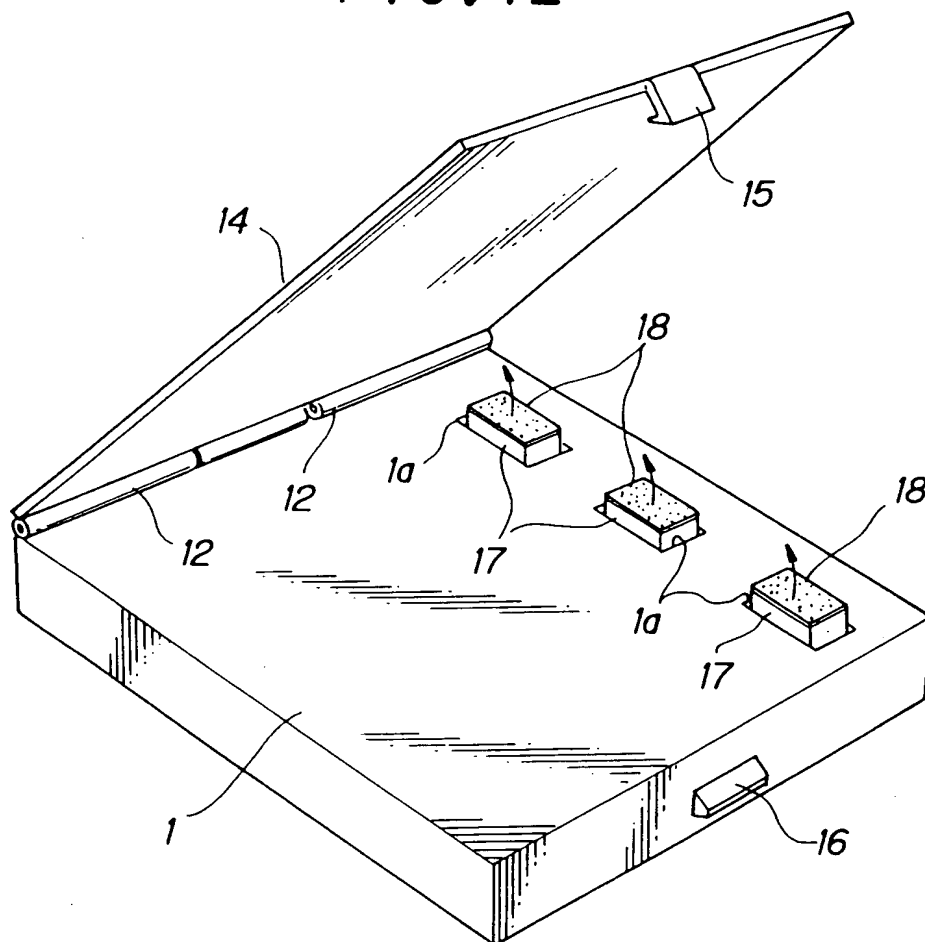


FIG. 13

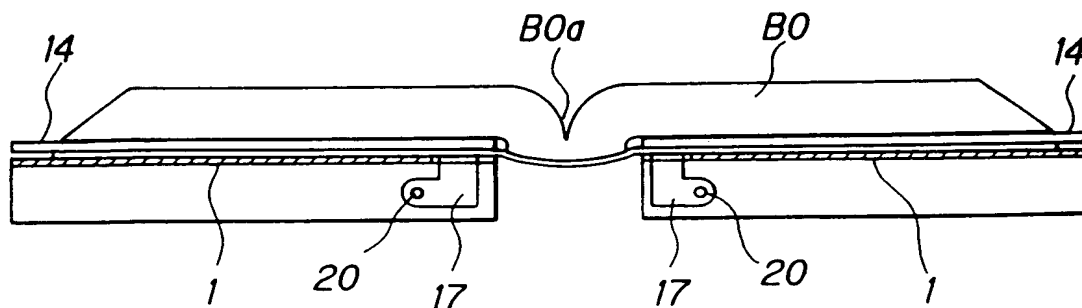


FIG. 14

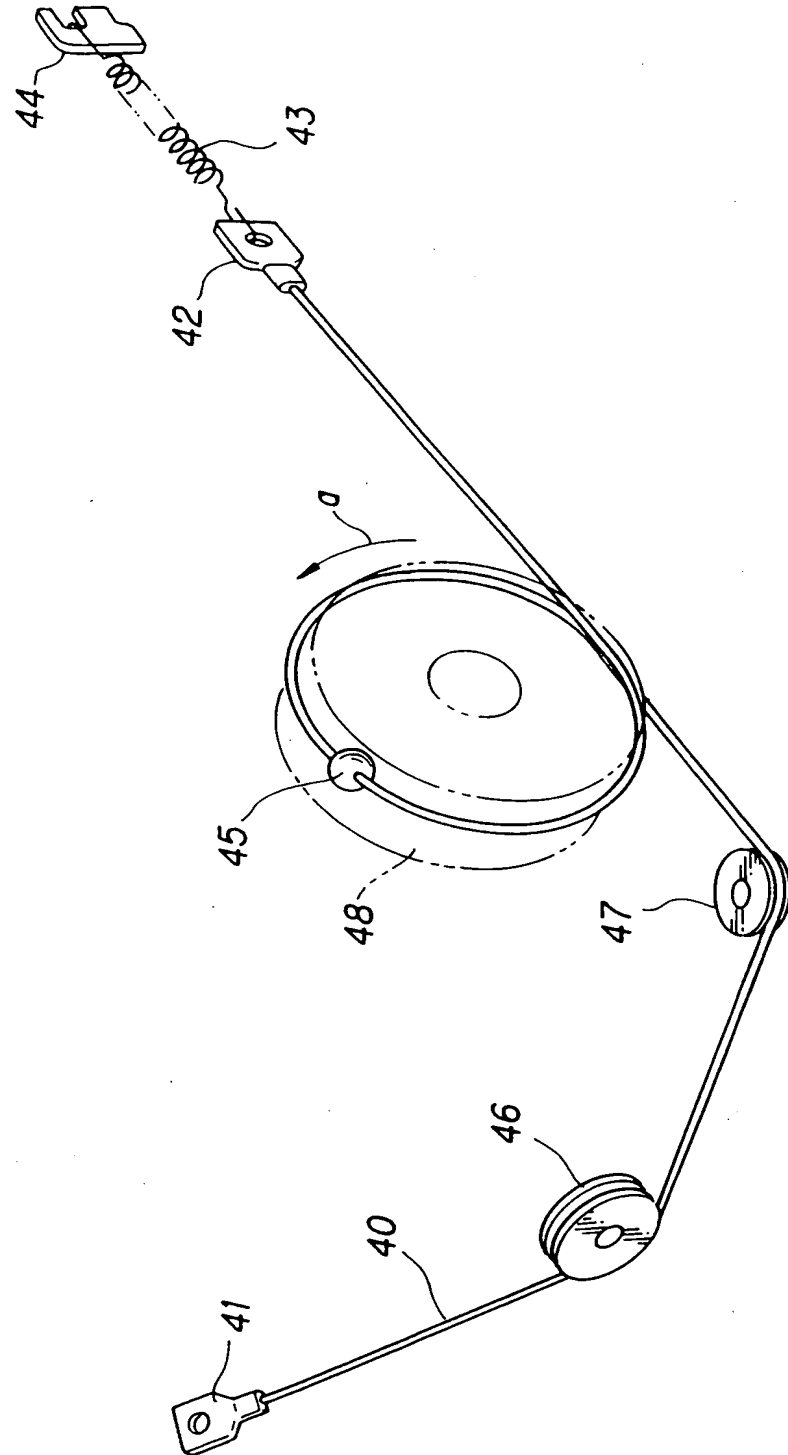


FIG. 15

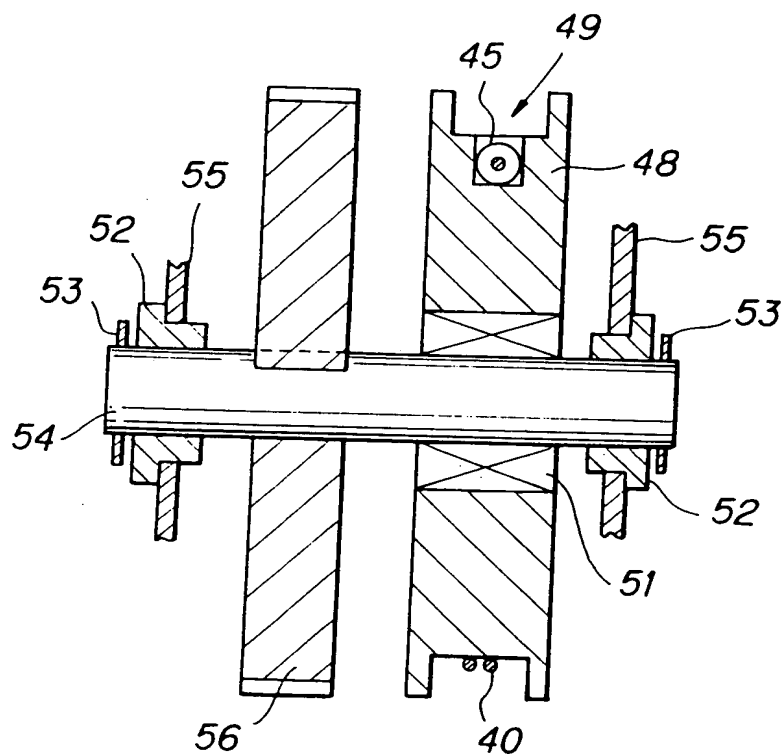


FIG. 16

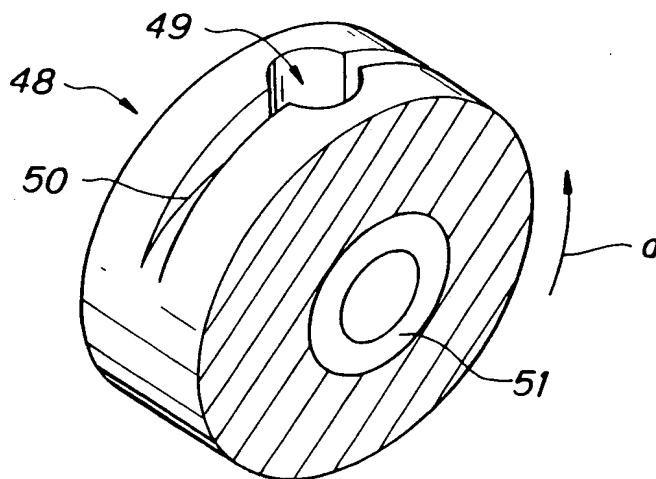


FIG. 17

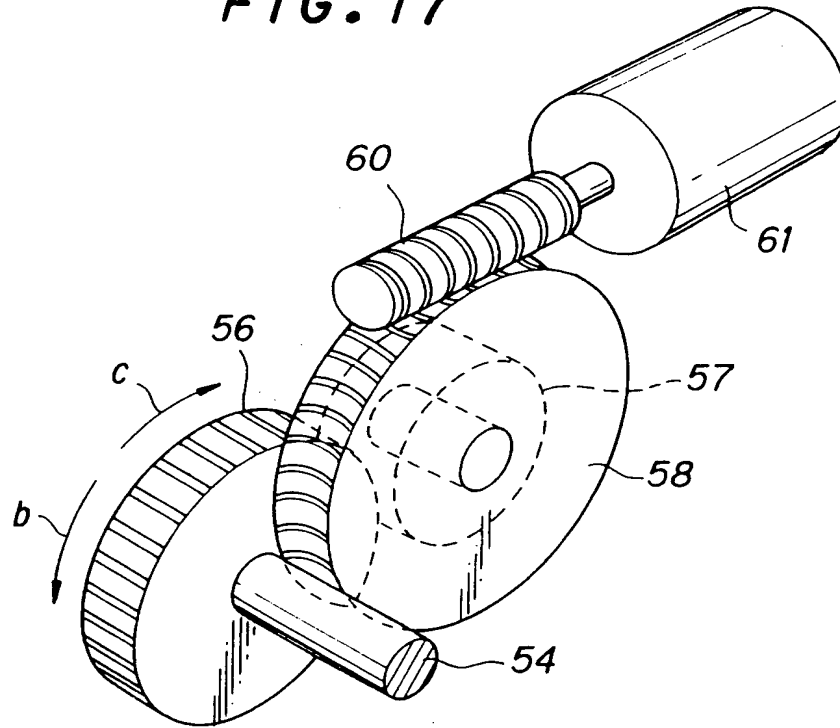


FIG. 18

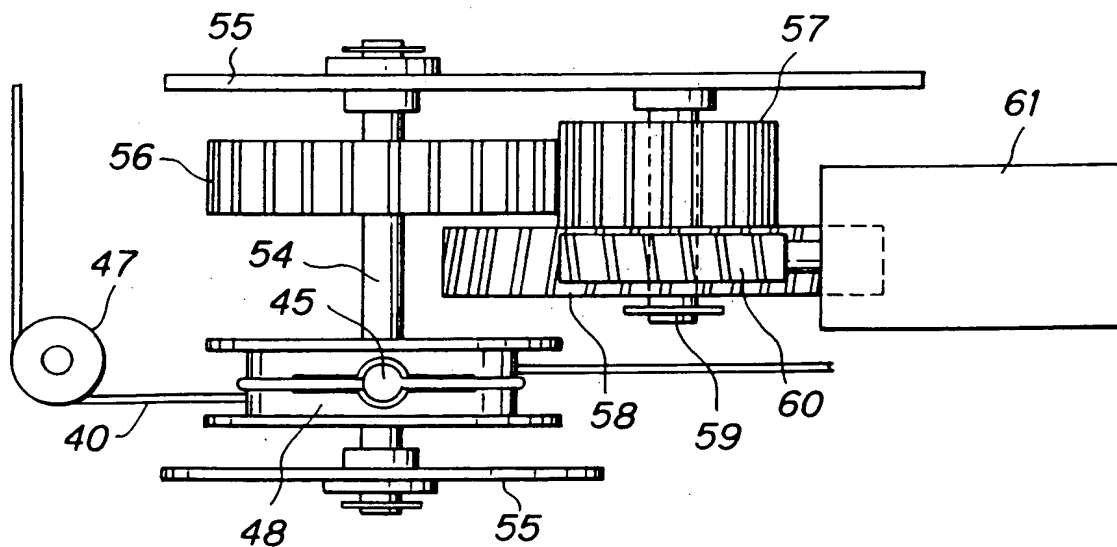


FIG. 19

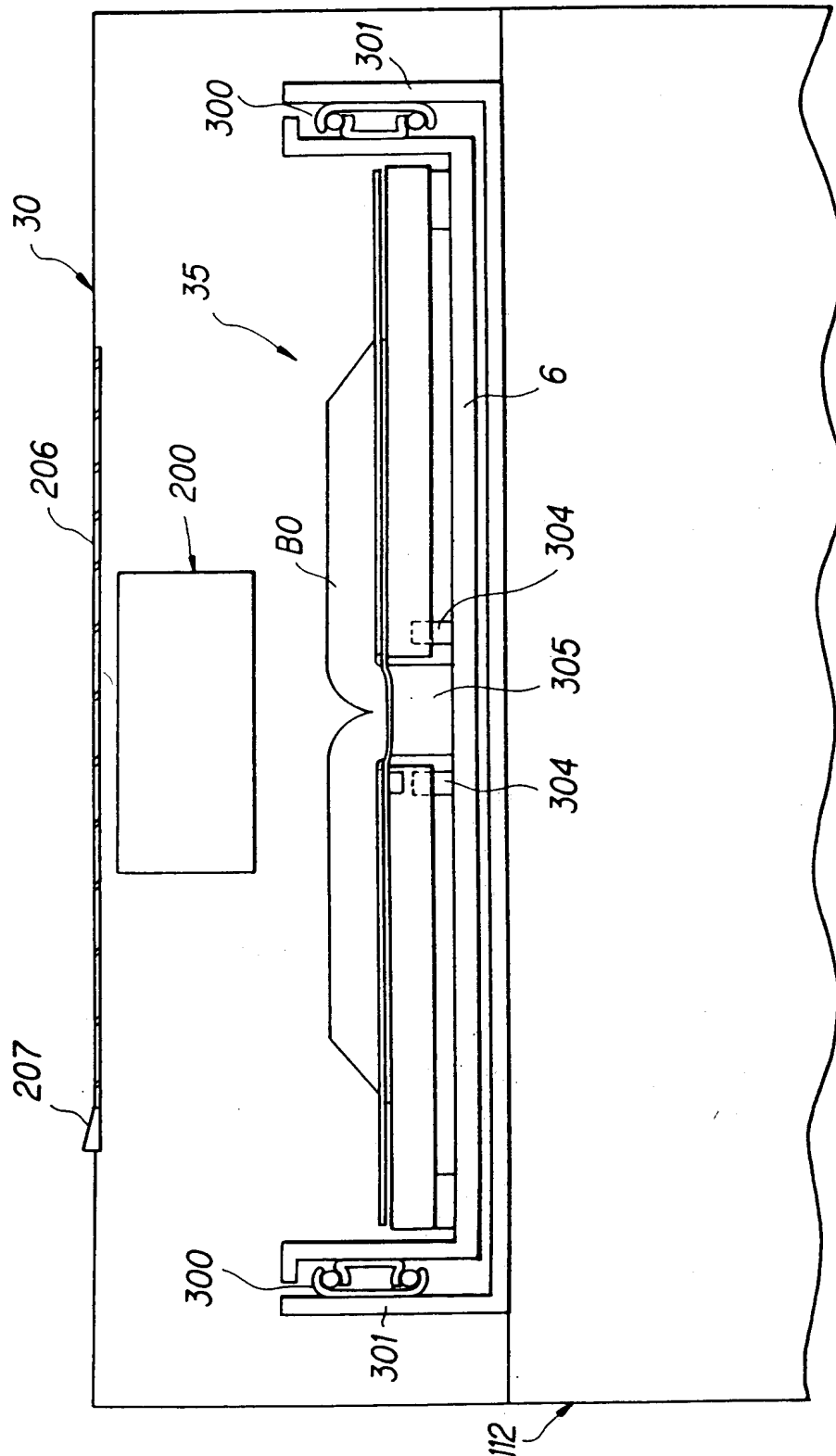


FIG. 20

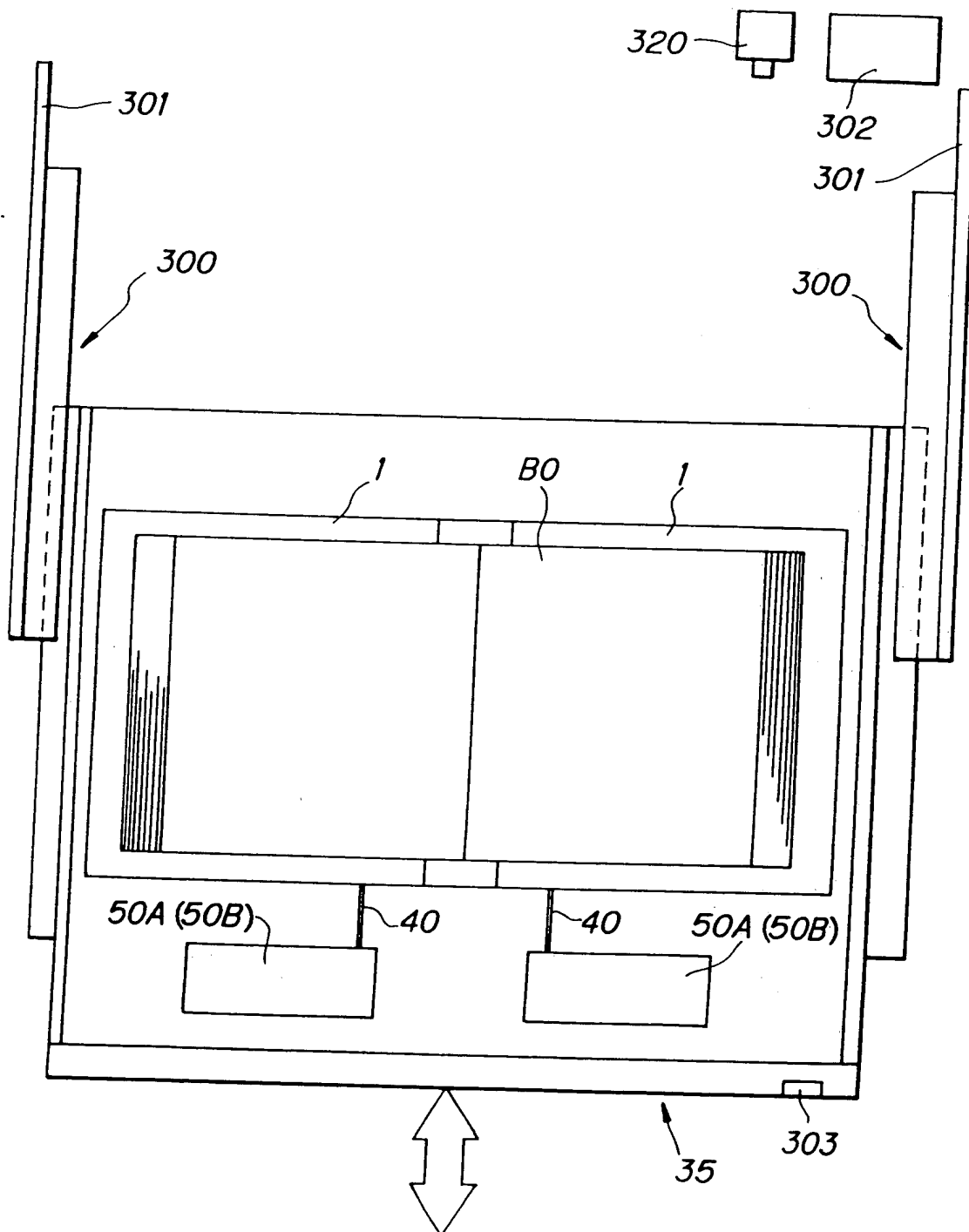


FIG. 21

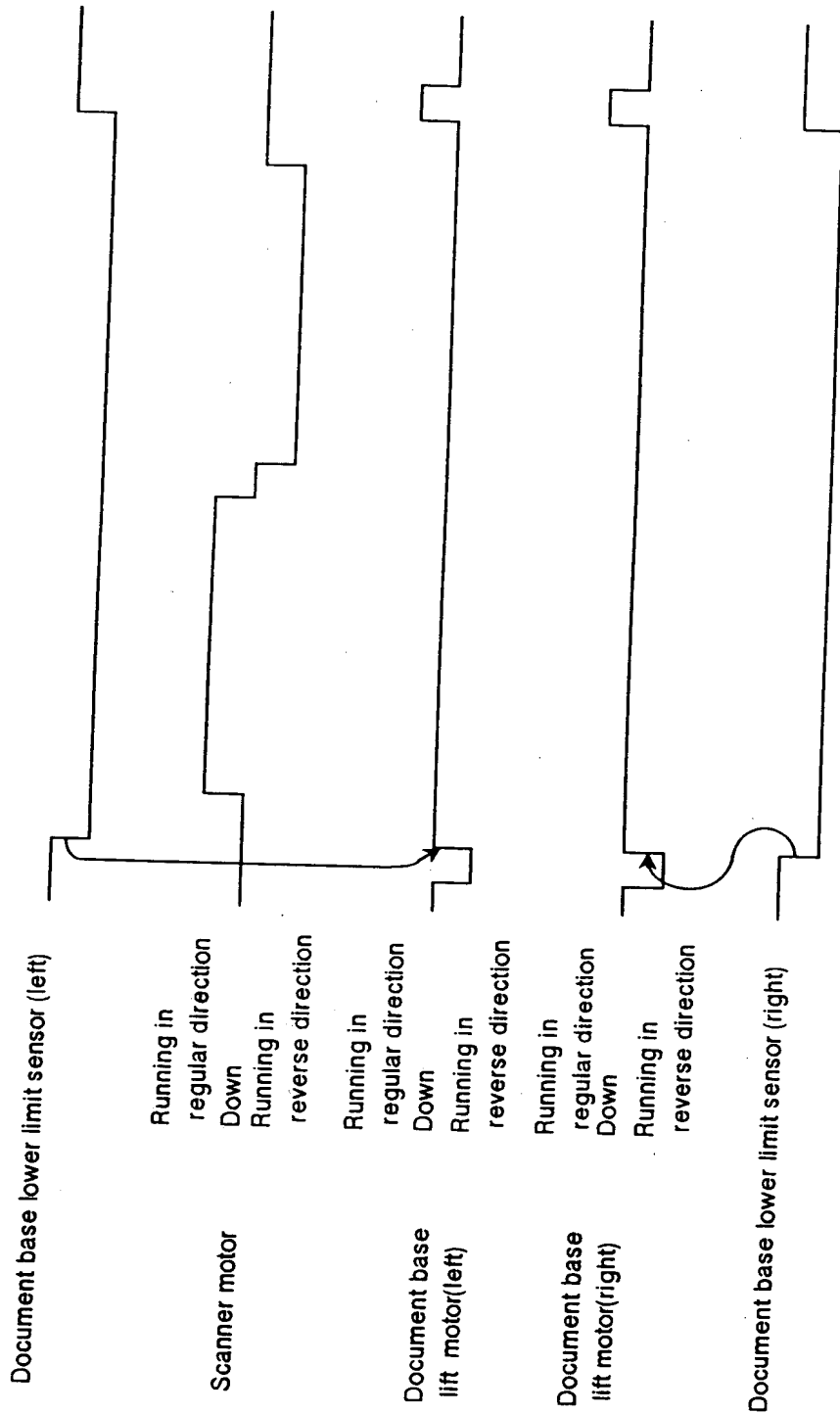


FIG. 23

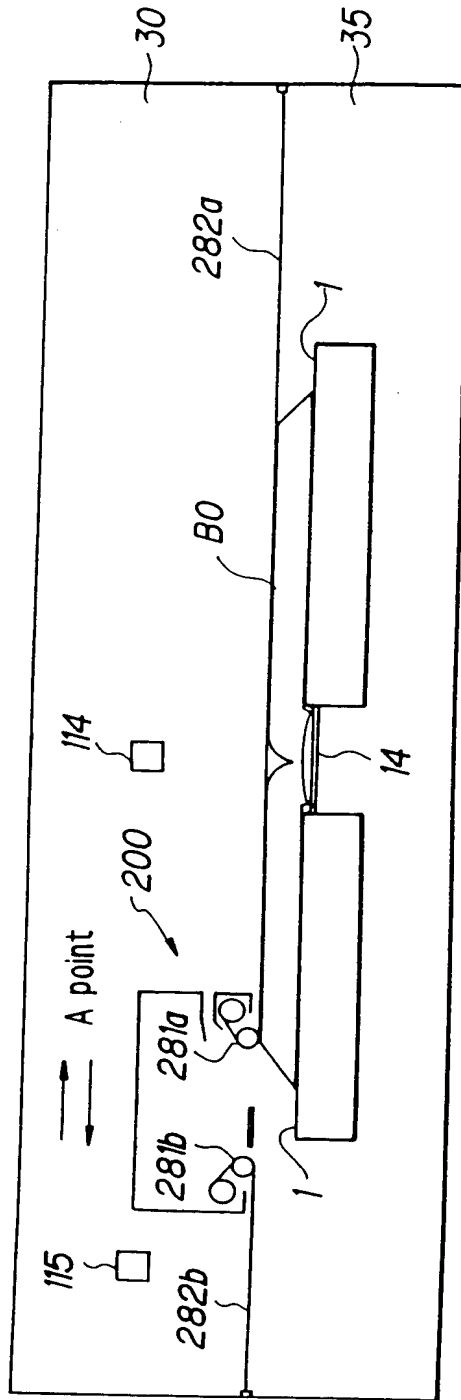


FIG. 24

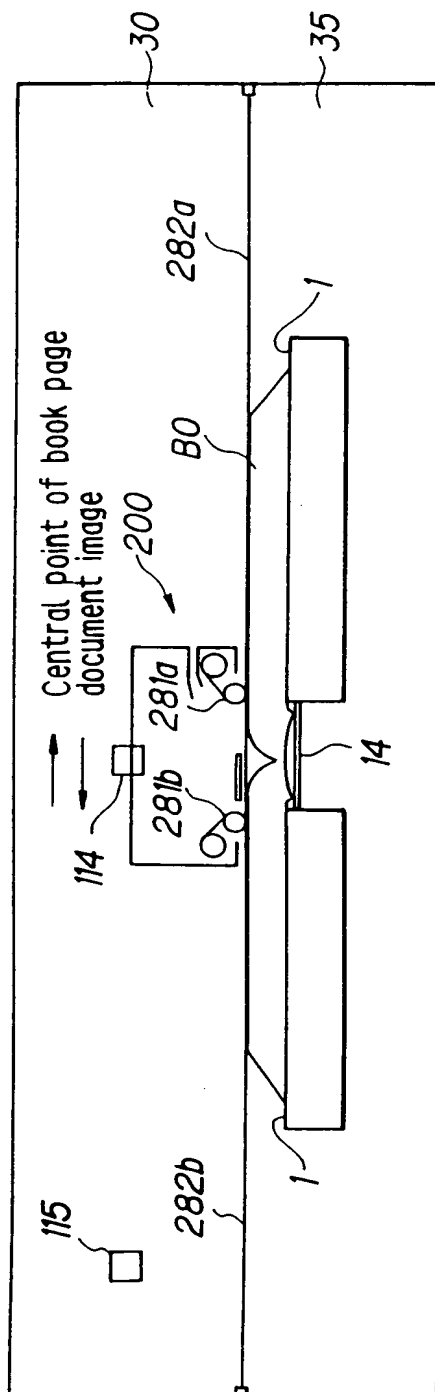


FIG. 25

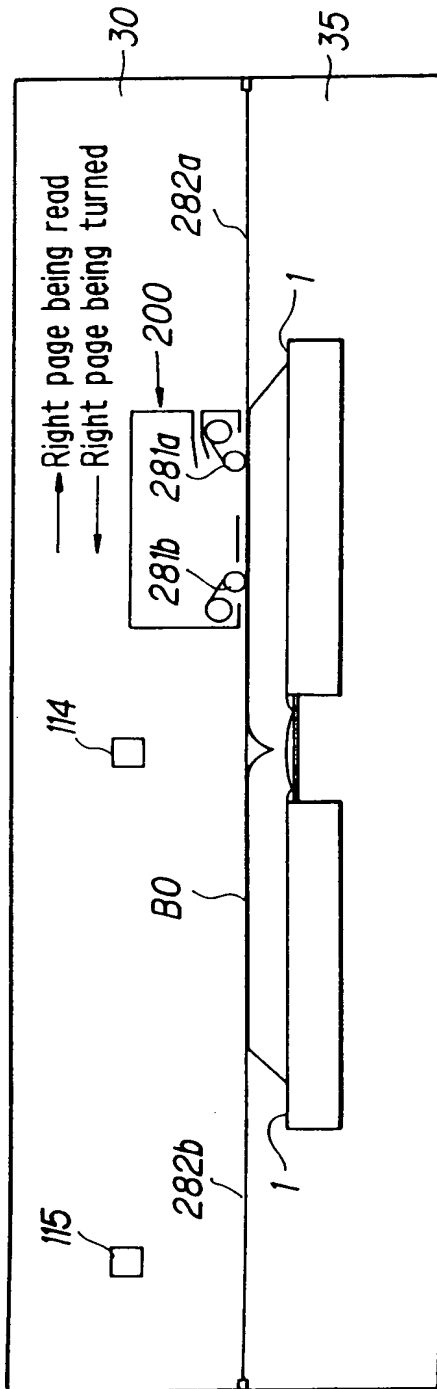


FIG. 26

23 / 50

OBLON, SPIVAK, ET AL
DOCKET #: 209560US-2-DIV
INV: Hiroshi TAKAHASHI
SHEET 23 OF 50

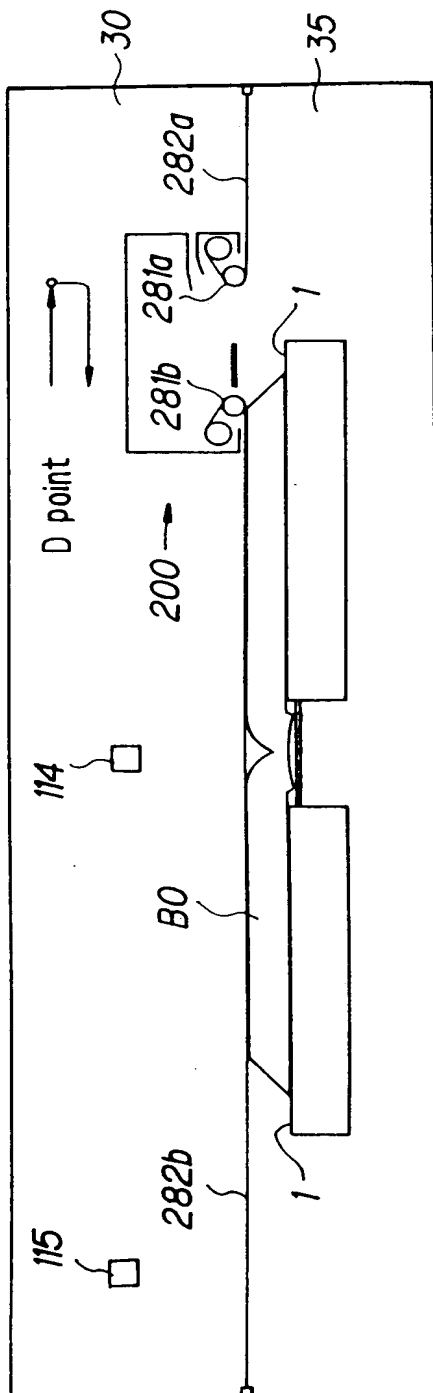


FIG.27

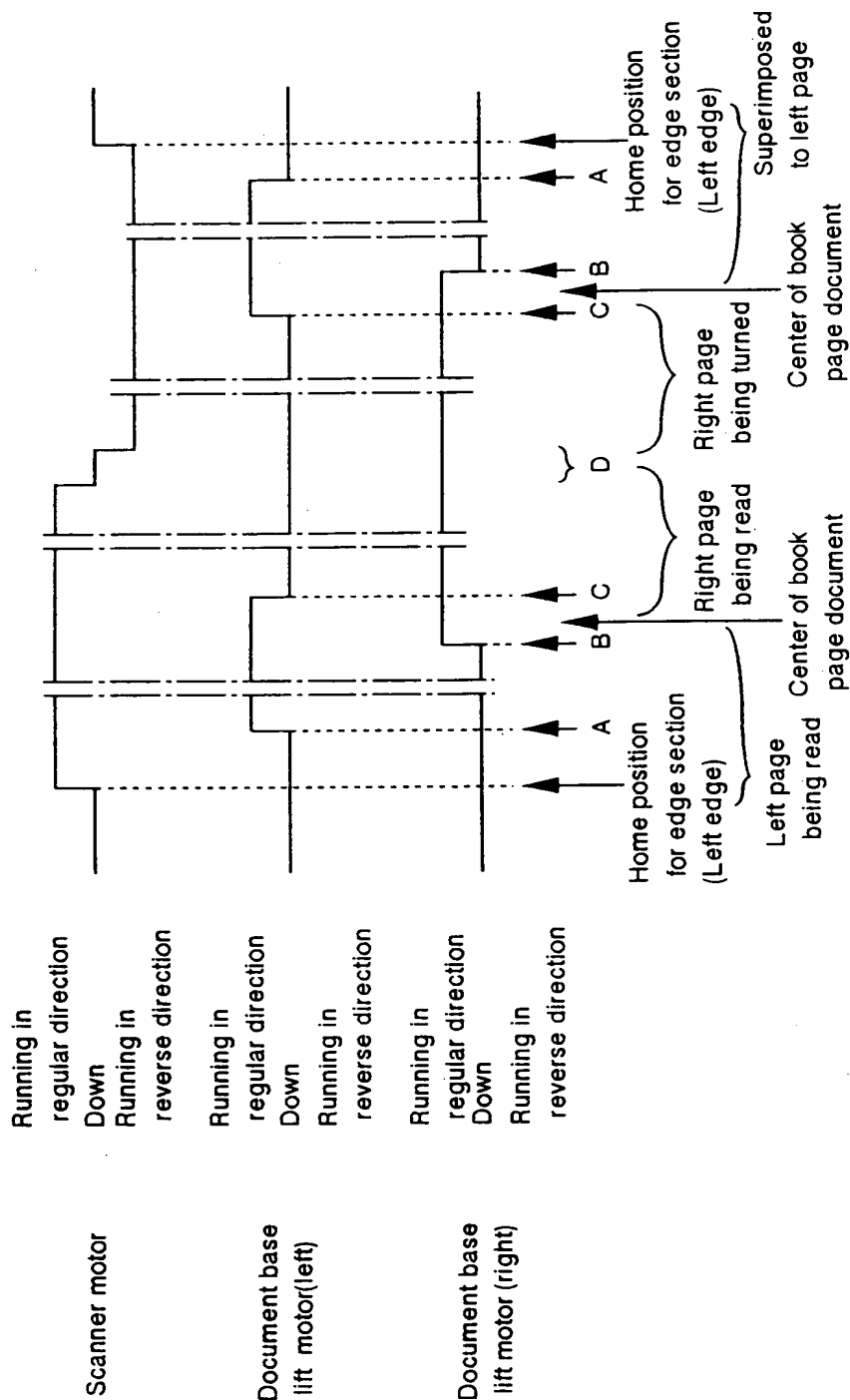
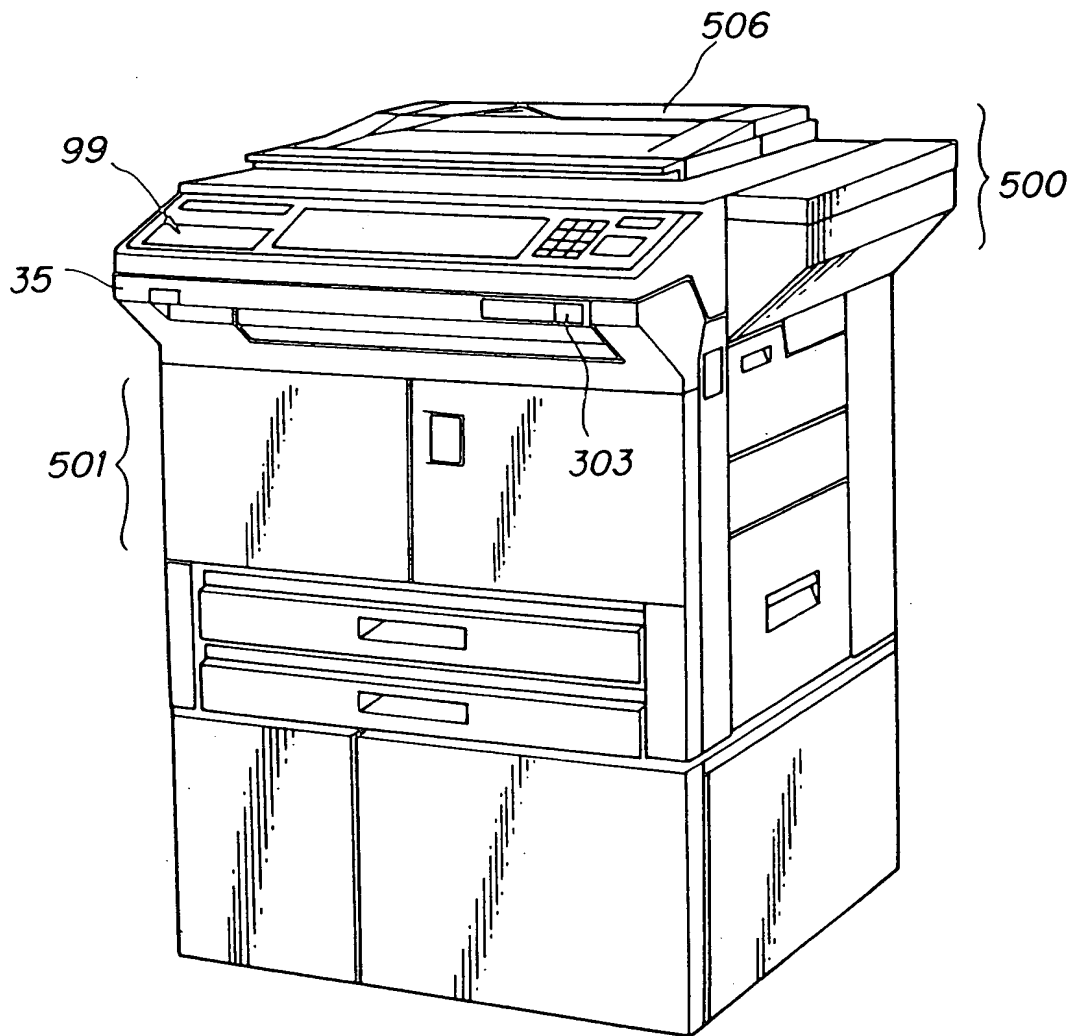


FIG. 28



0956911.092101

FIG. 29

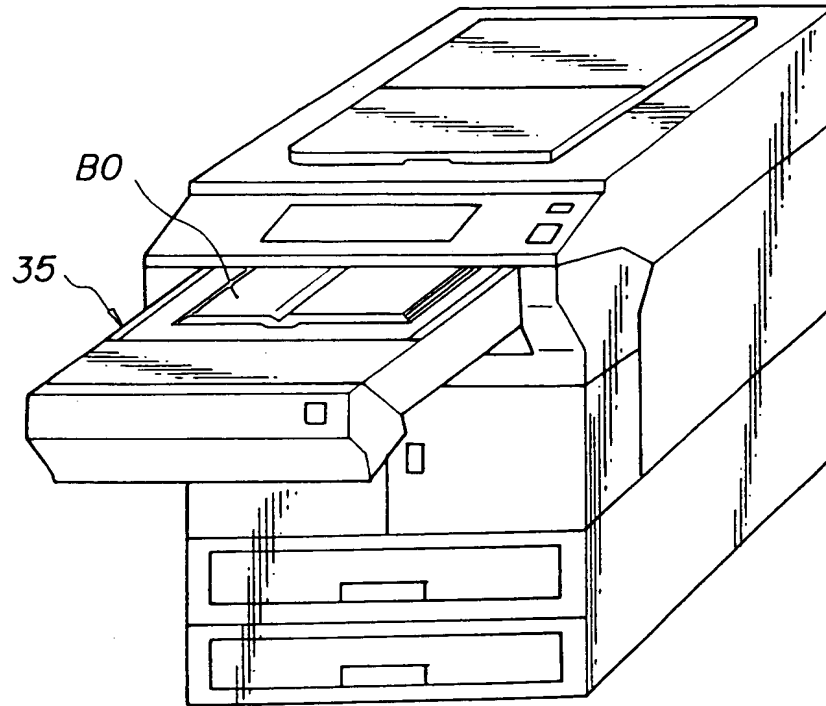


FIG. 30

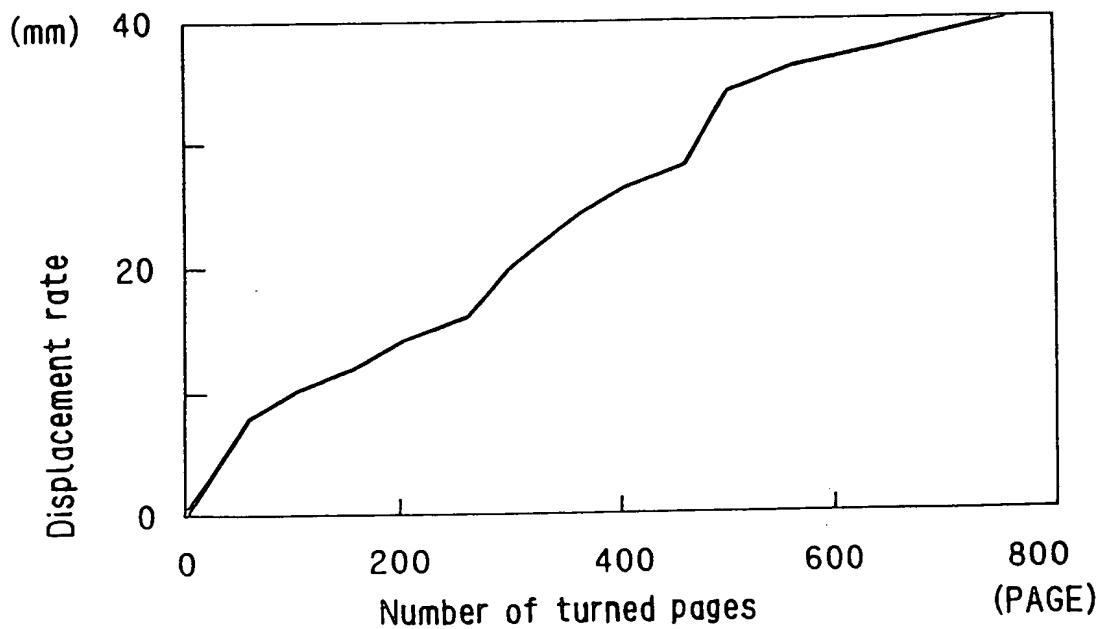


FIG. 31

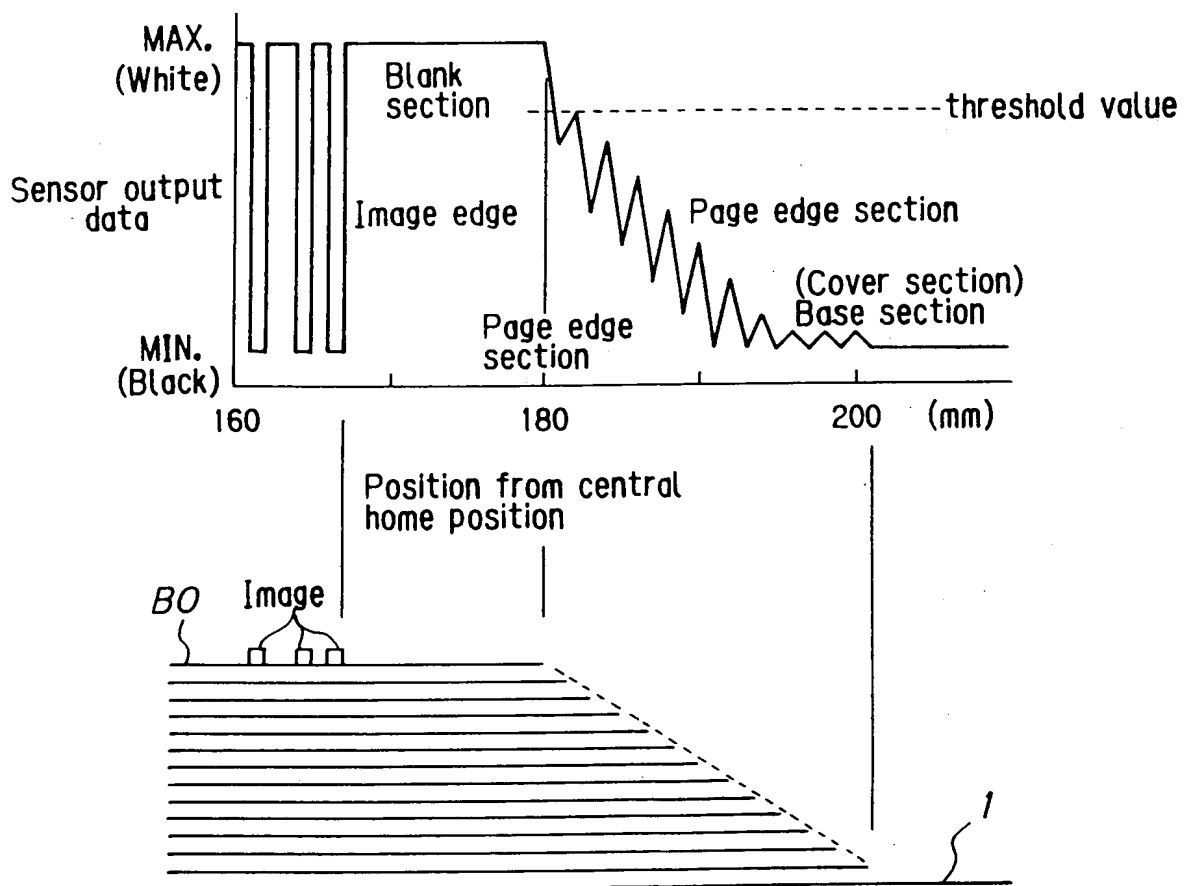
095601.09101
TOT 260 TTS 560

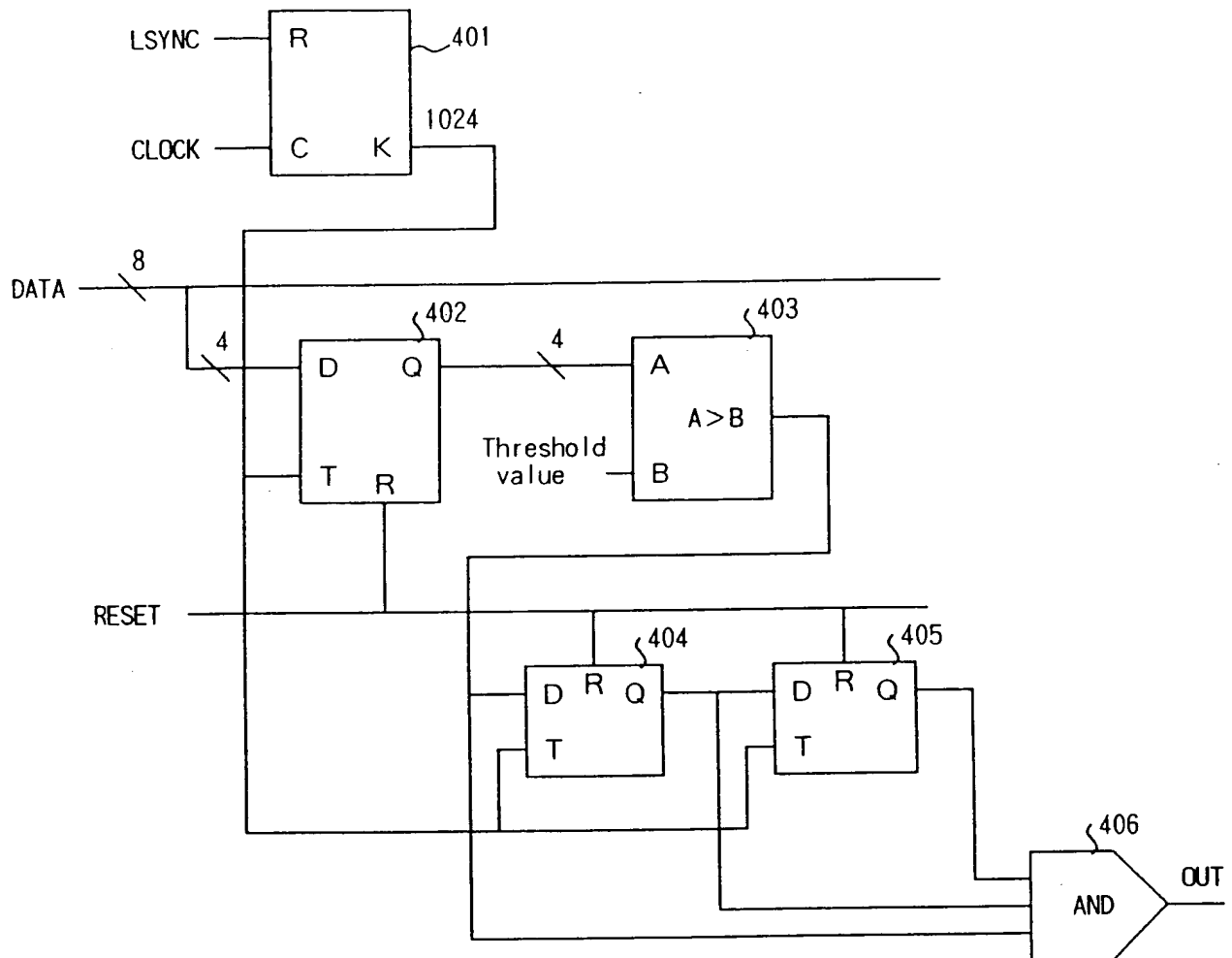
FIG.32

FIG. 33

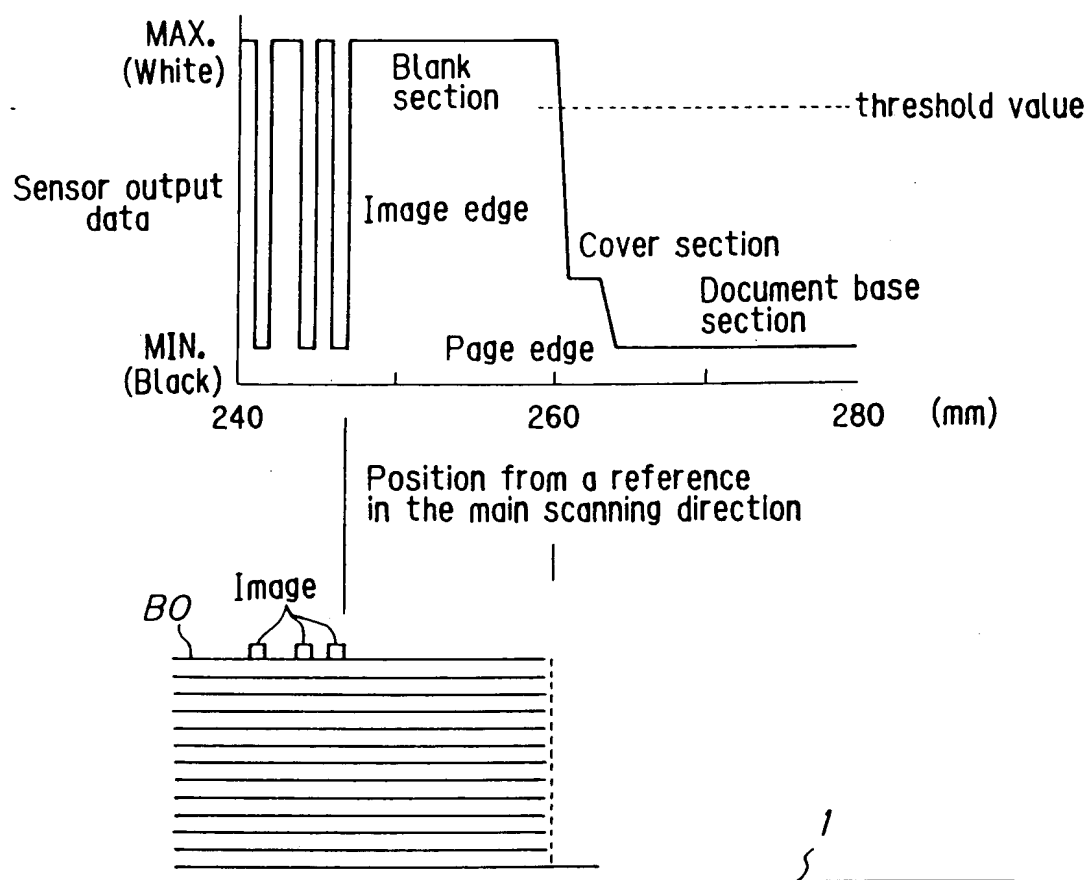


FIG. 34

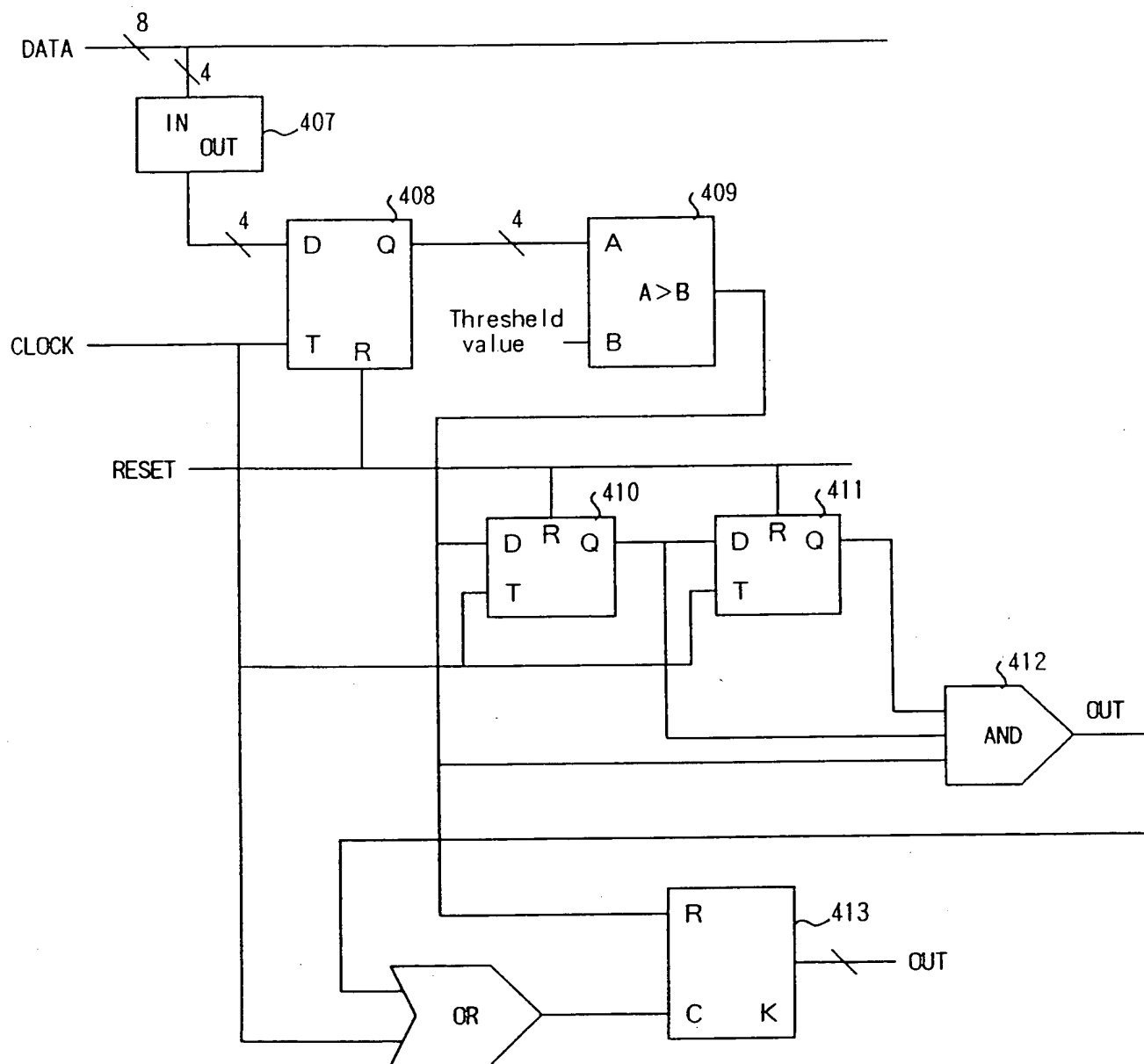
09956011.092101
"T0T260" T169560

FIG. 35

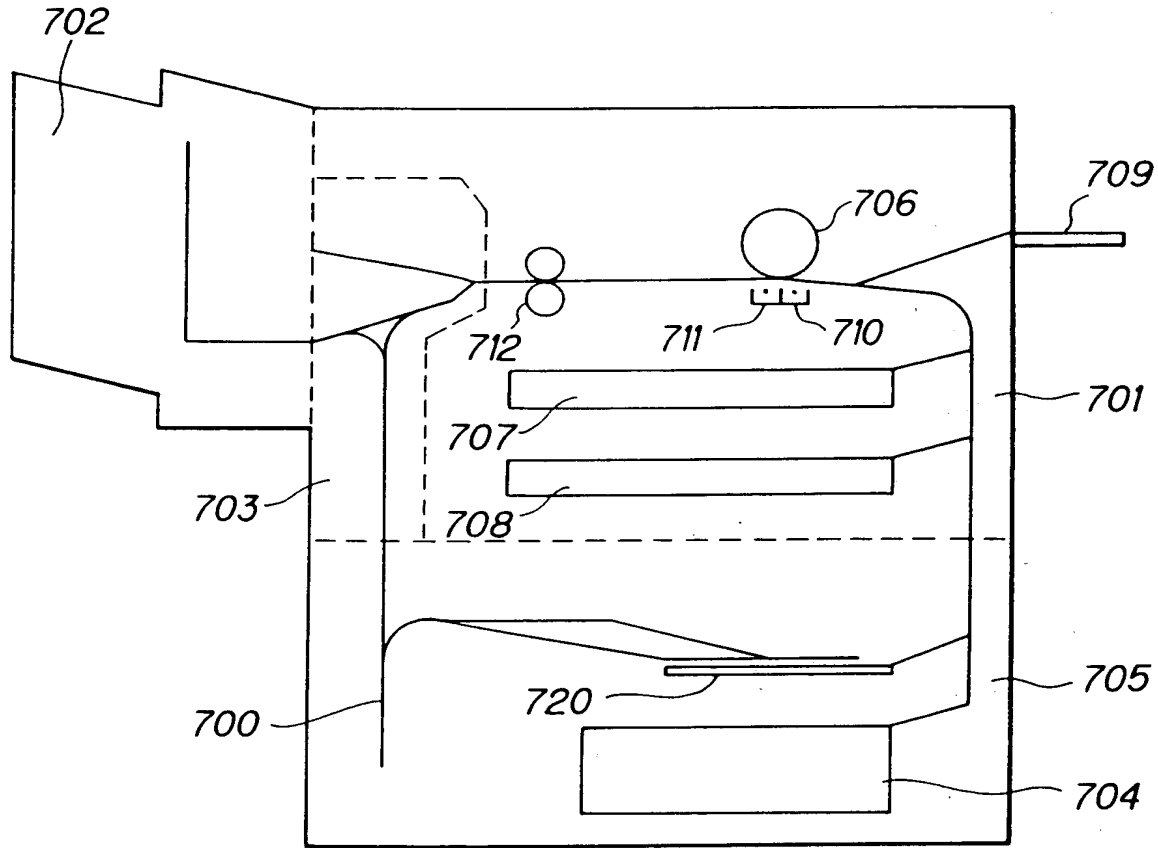


FIG. 36

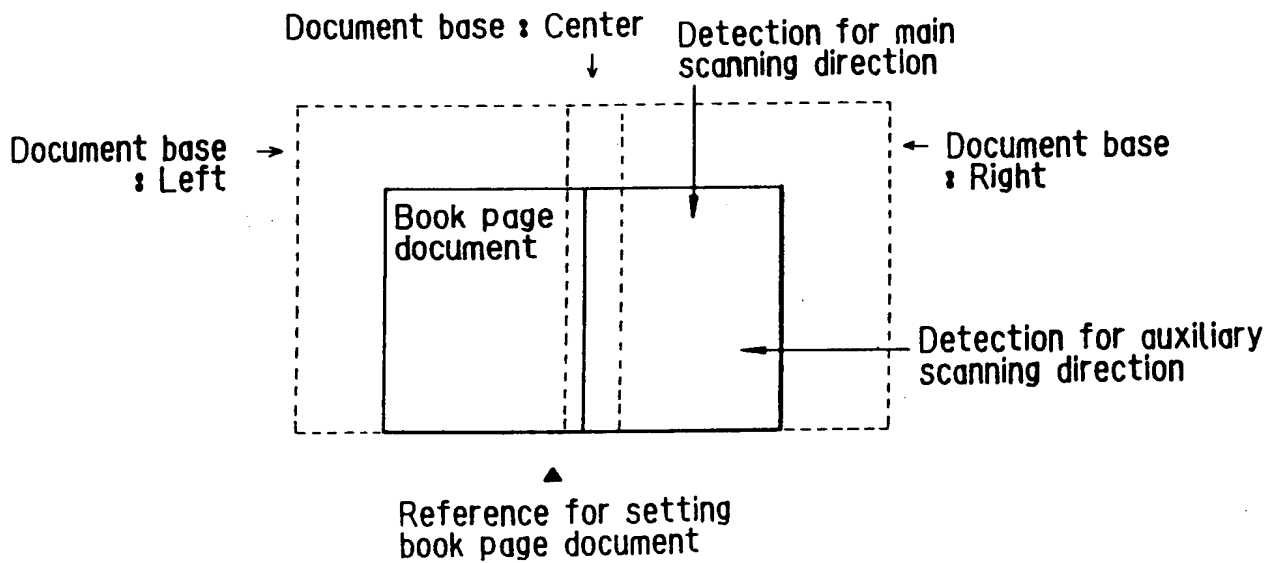


FIG. 37

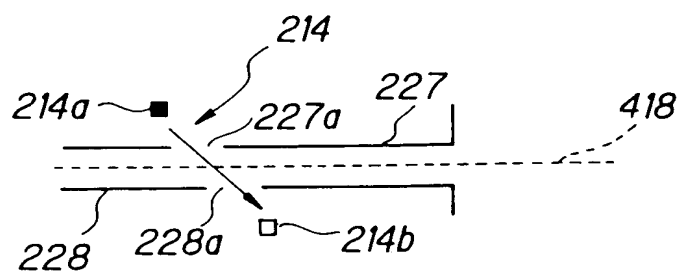


FIG. 38A

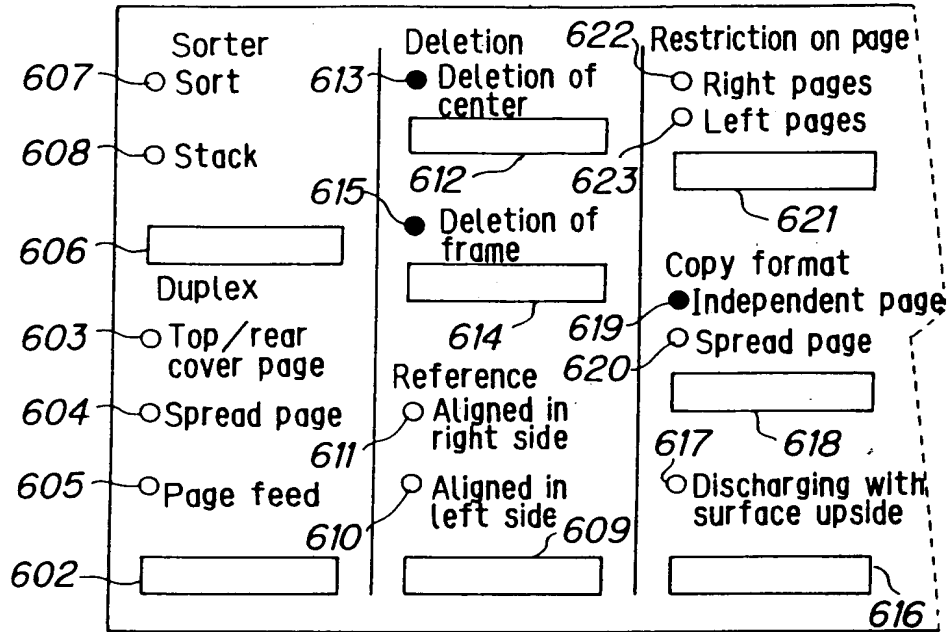


FIG. 38B

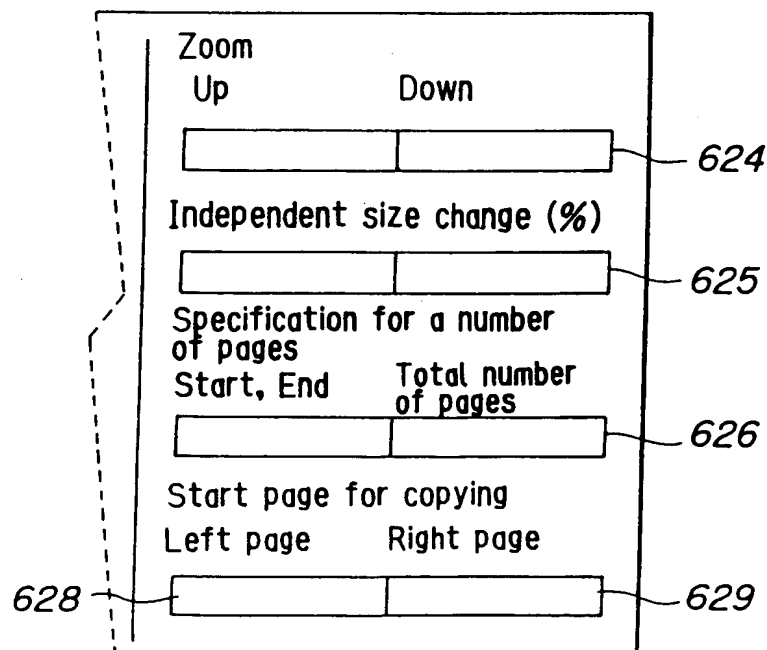


FIG. 39A

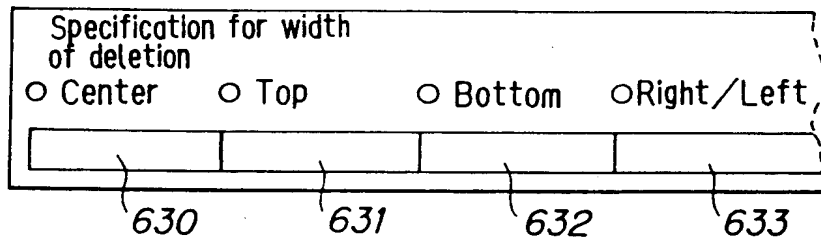


FIG. 39B

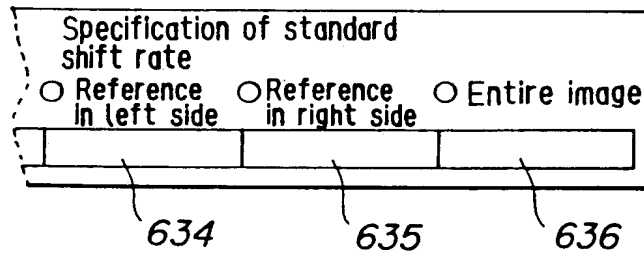


FIG. 40

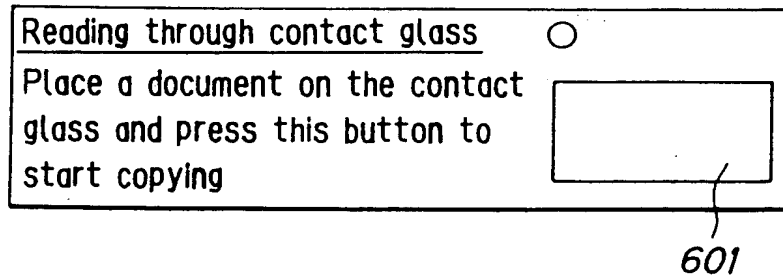


FIG. 41

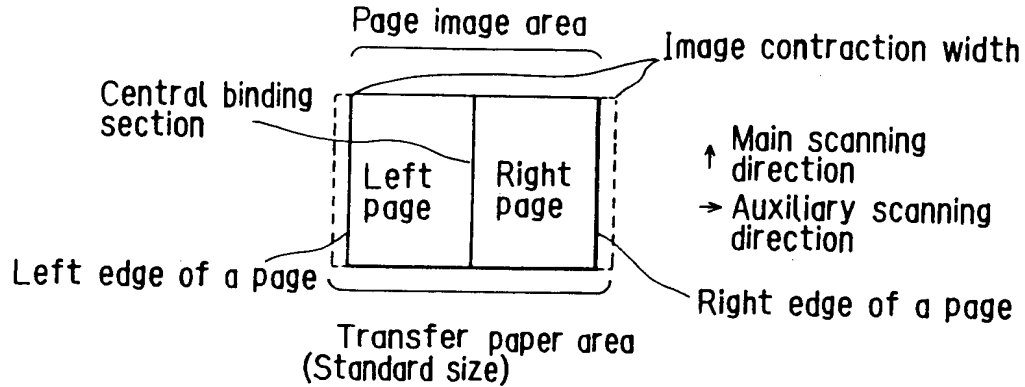


FIG. 42A

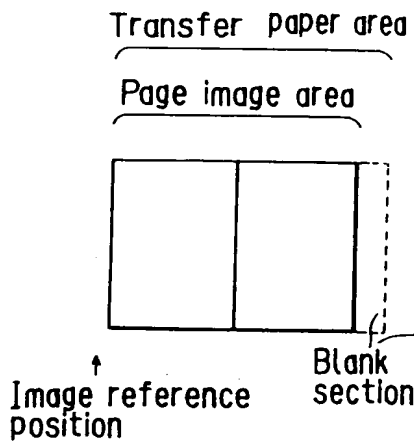


FIG. 42B

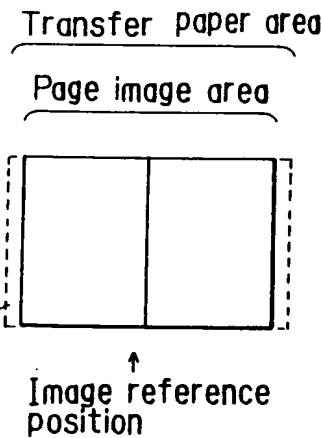


FIG. 42C

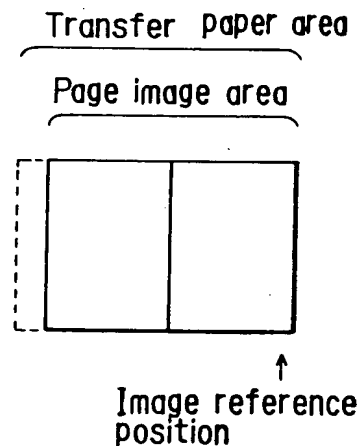


FIG. 43A

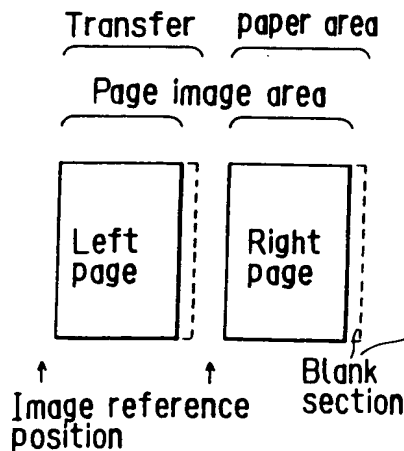


FIG. 43B

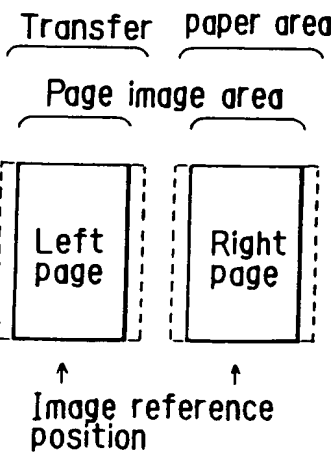
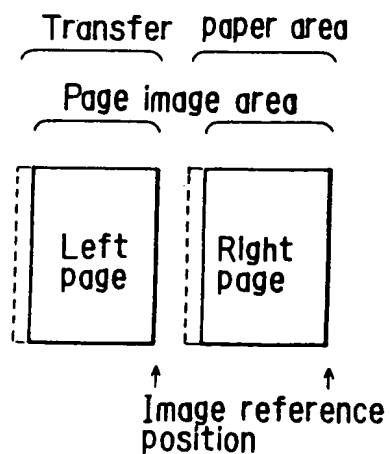
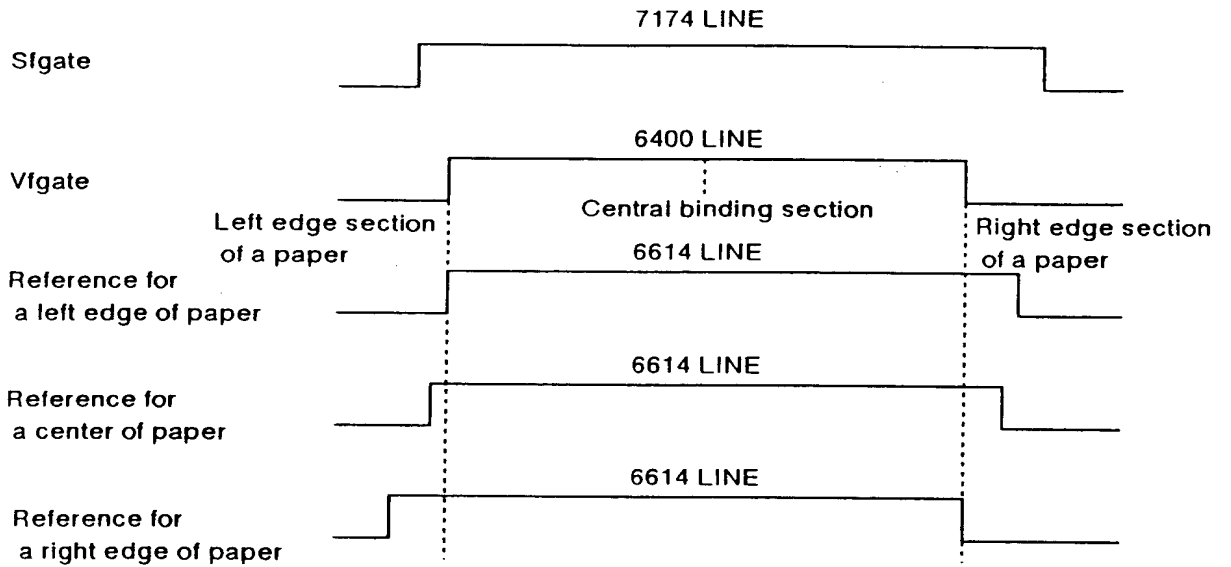


FIG. 43C

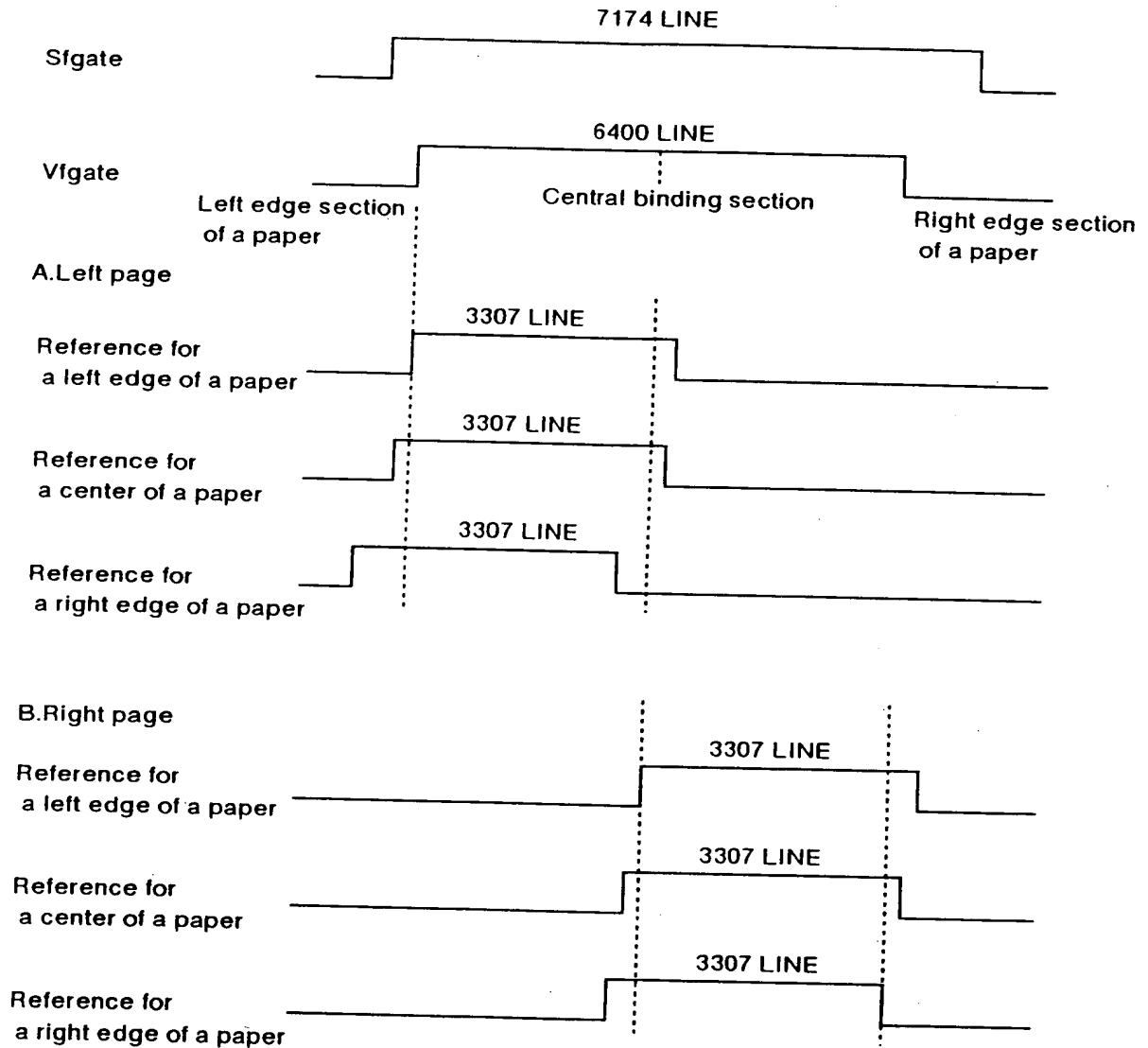


099560US-2-DIV

FIG.44



209560US-2-DIV

FIG.45

209560US-2-DIV

FIG.46

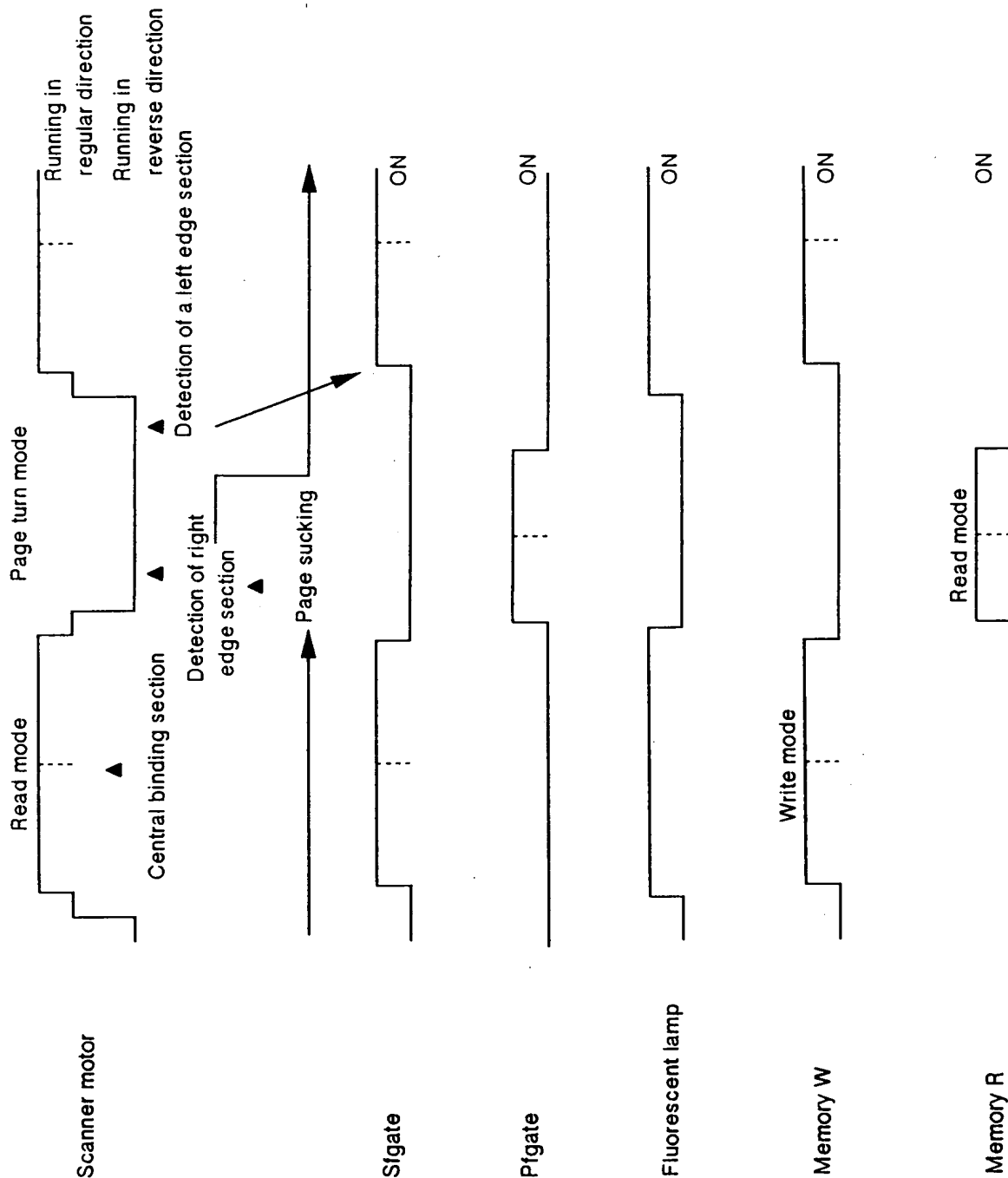


FIG.47

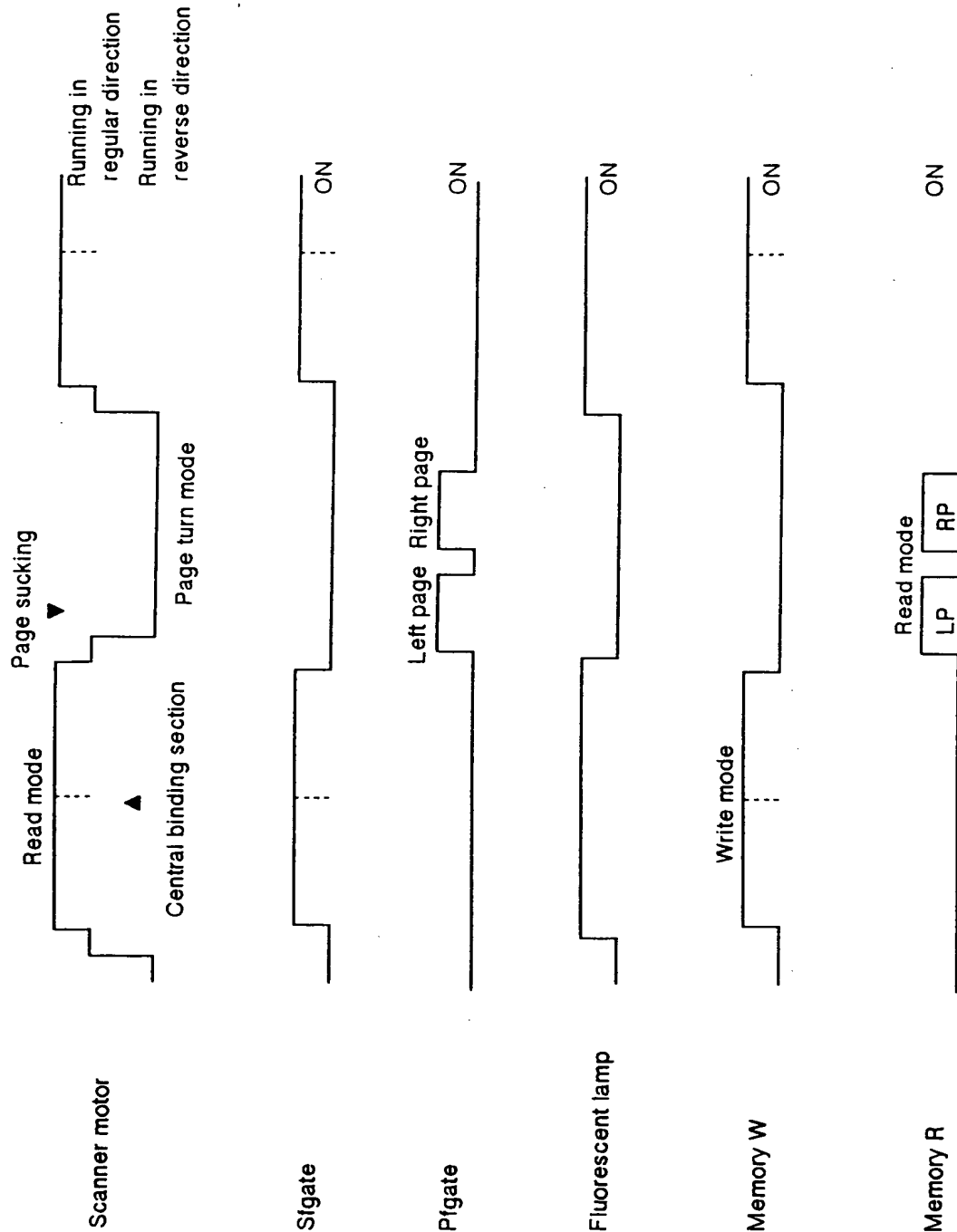


FIG. 48A

Bound Mono		100%
Right P.	15 - 48	P. 24

FIG. 48B

Bound Mono		100%
P.	1 - 40	

FIG. 48C

Sheet	100%
-------	------

FIG. 48D

Memory range for expansion over

09564109210
1012601169660

FIG. 49

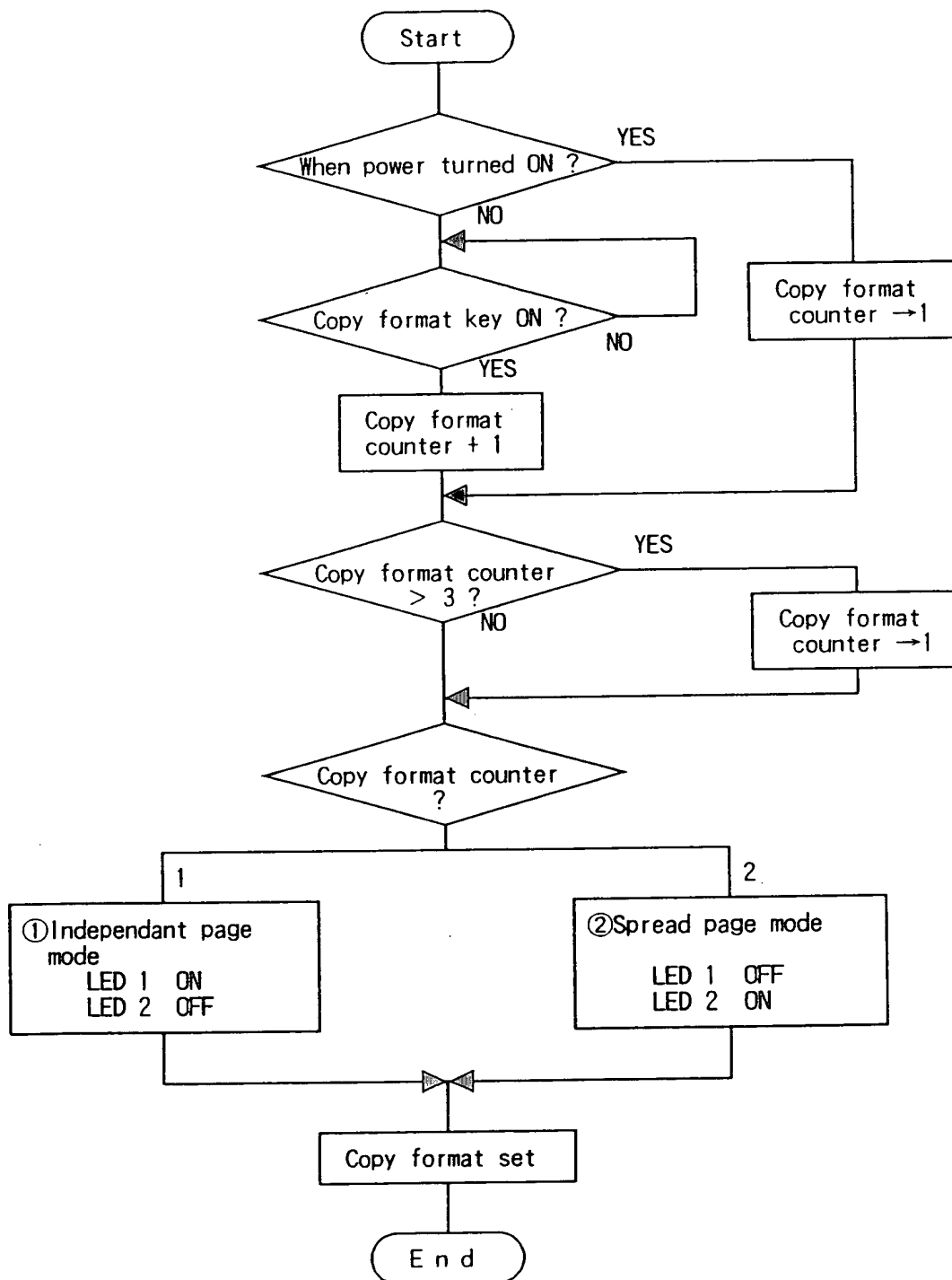


FIG. 50

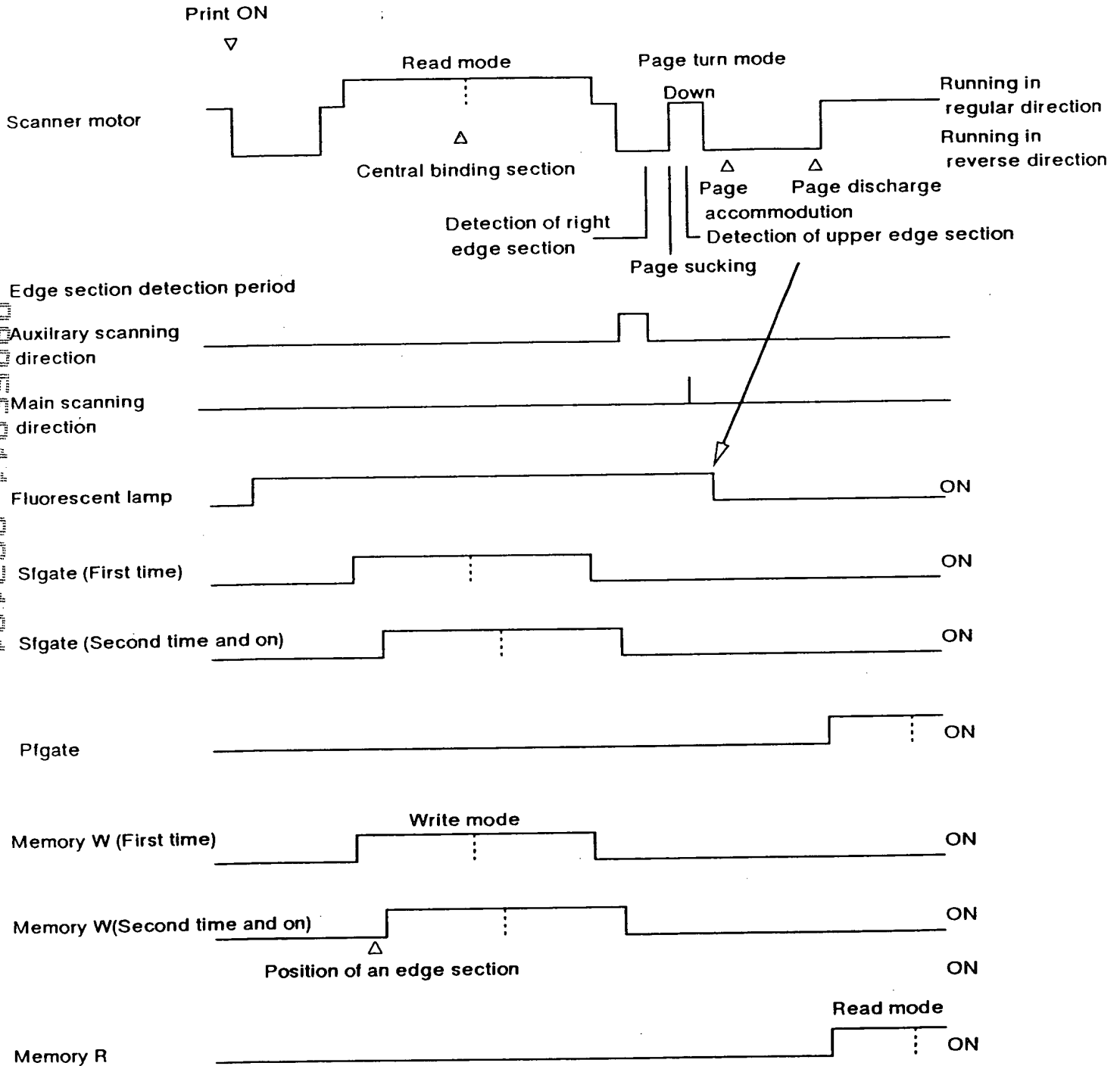


FIG. 51

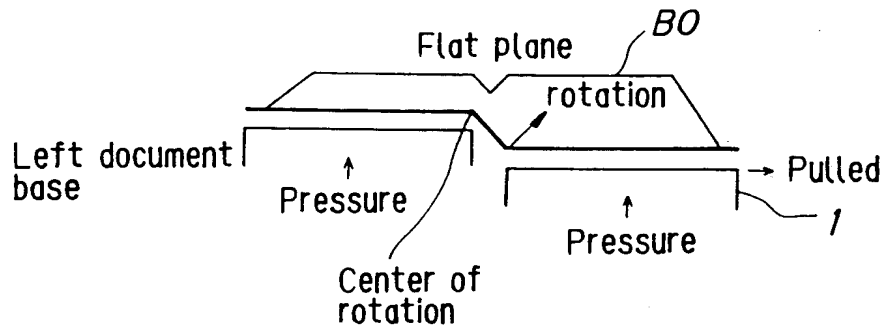


FIG. 52

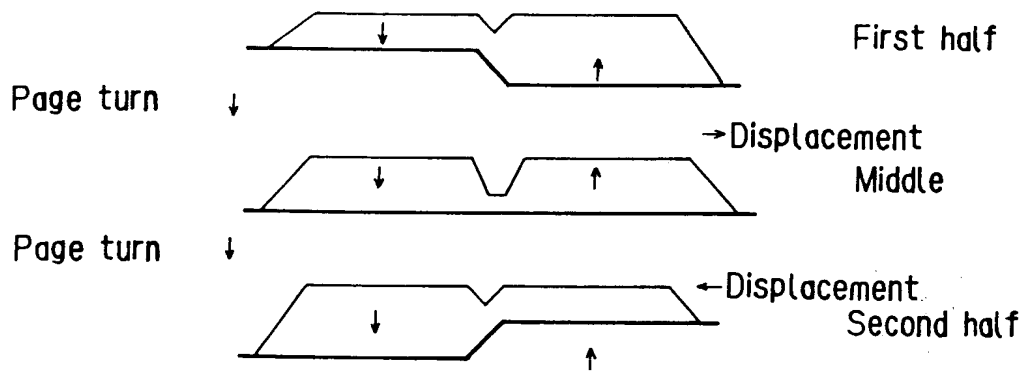


Image memory range

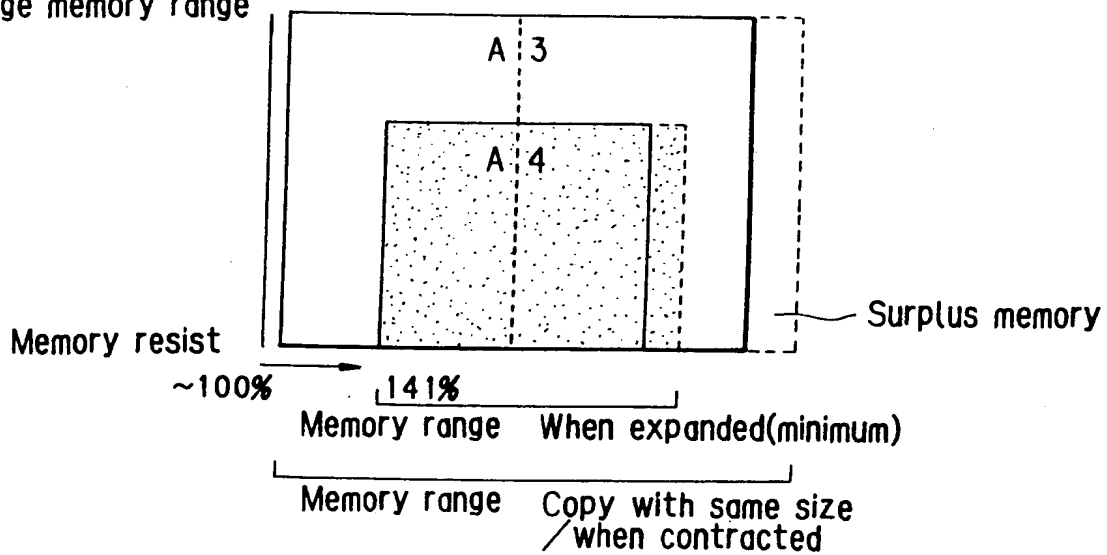


FIG.53

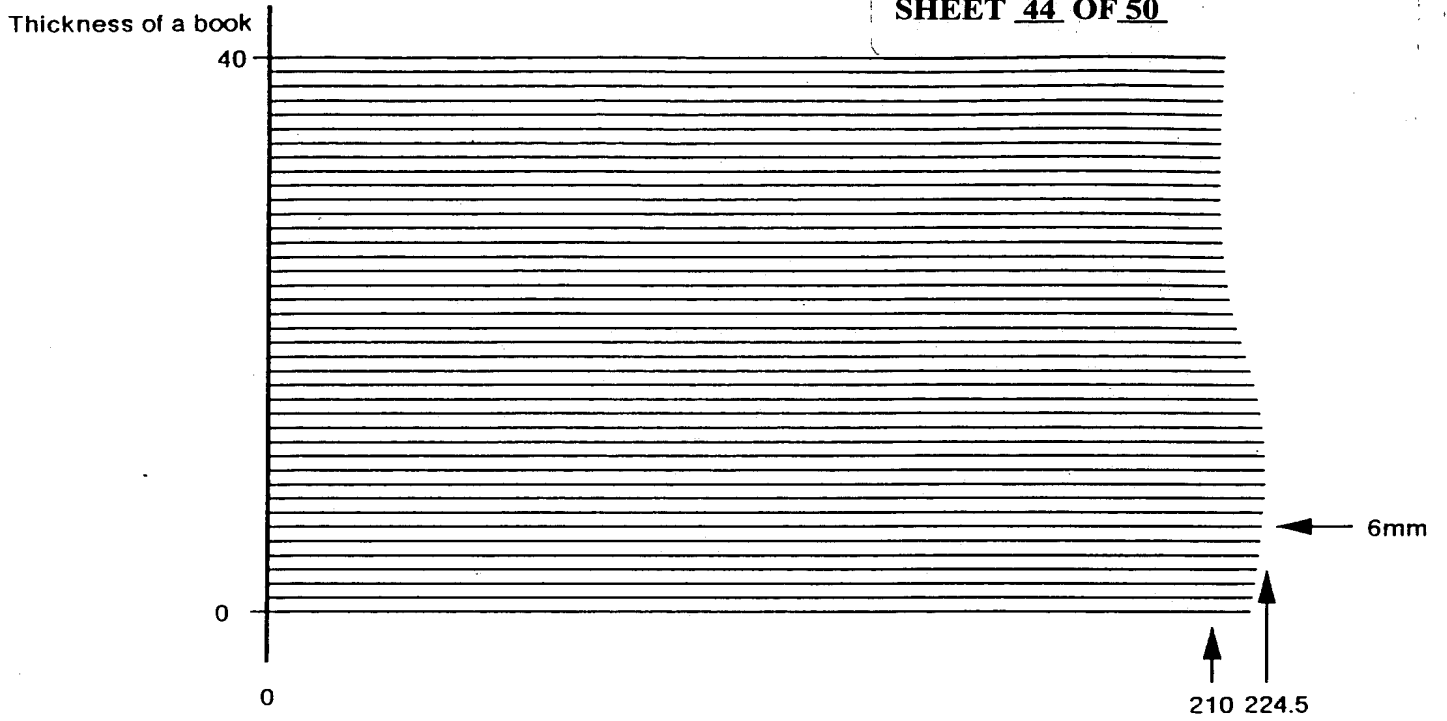
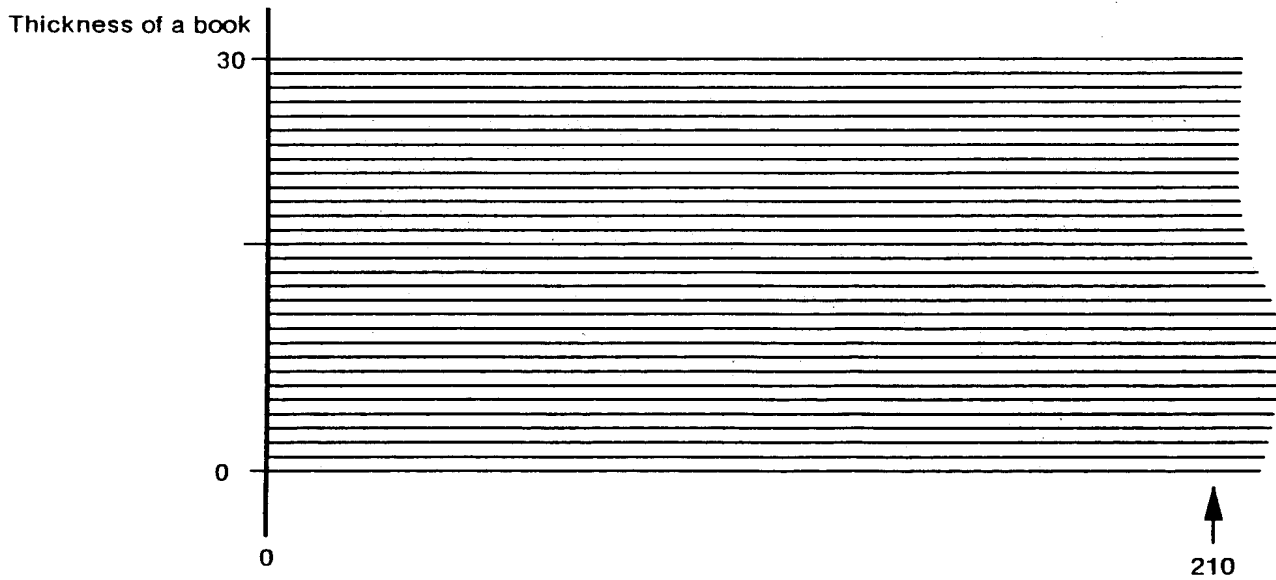


FIG.54



209560US-2-DIV

FIG.55

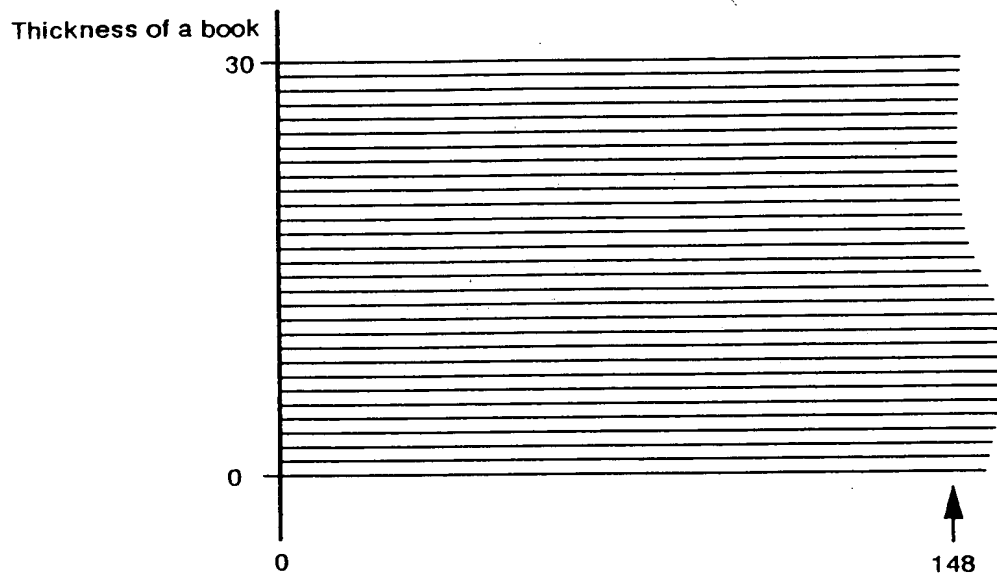


FIG.56

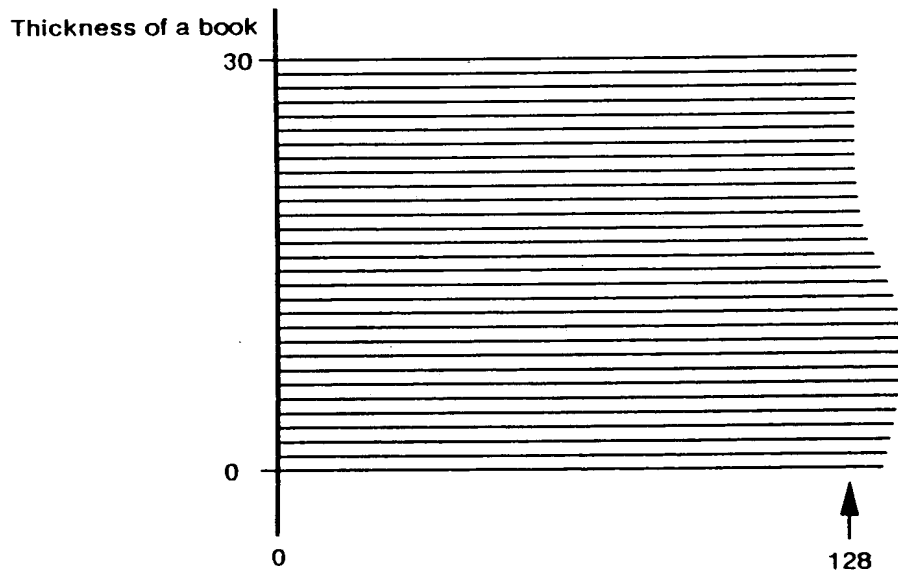


FIG. 55

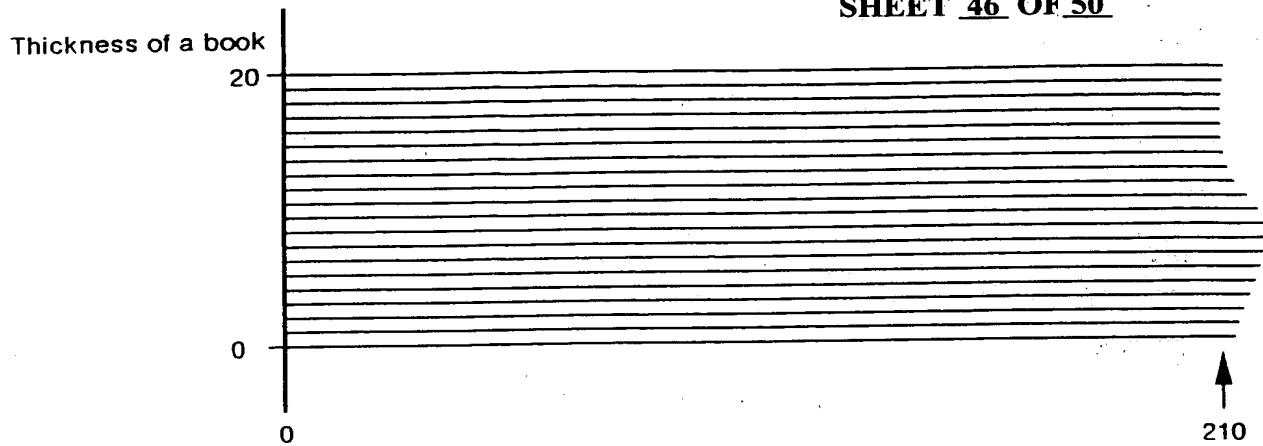
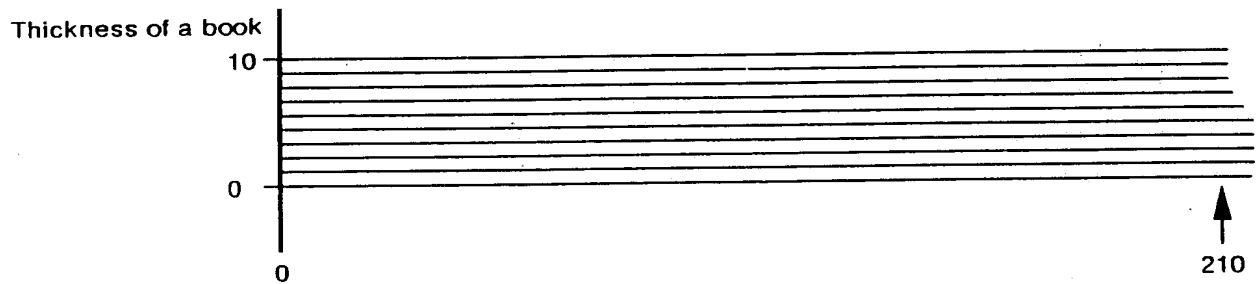
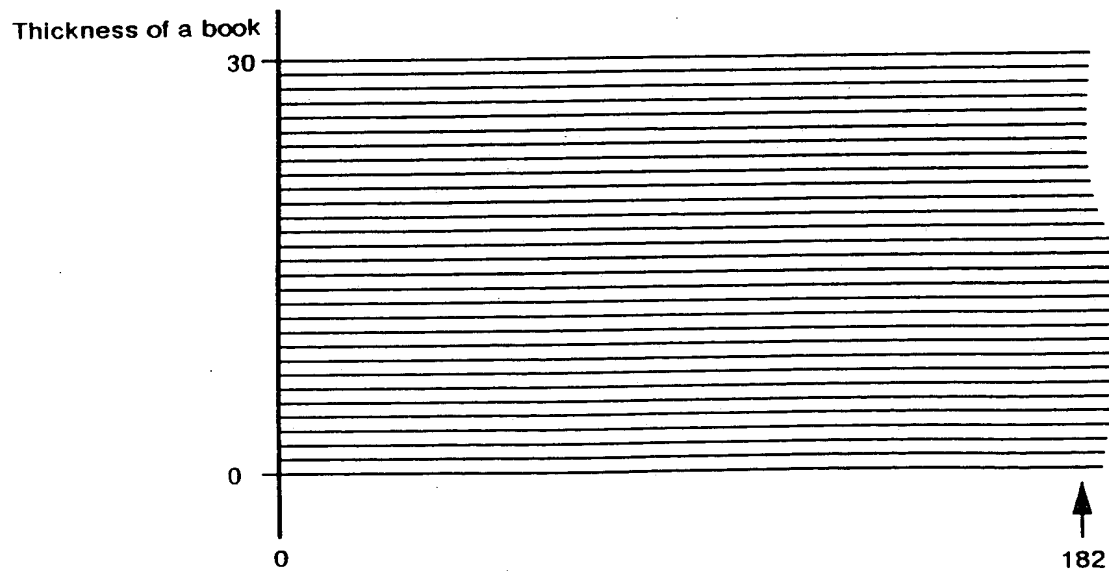
FIG.57**FIG.58****FIG.59**

FIG. 60

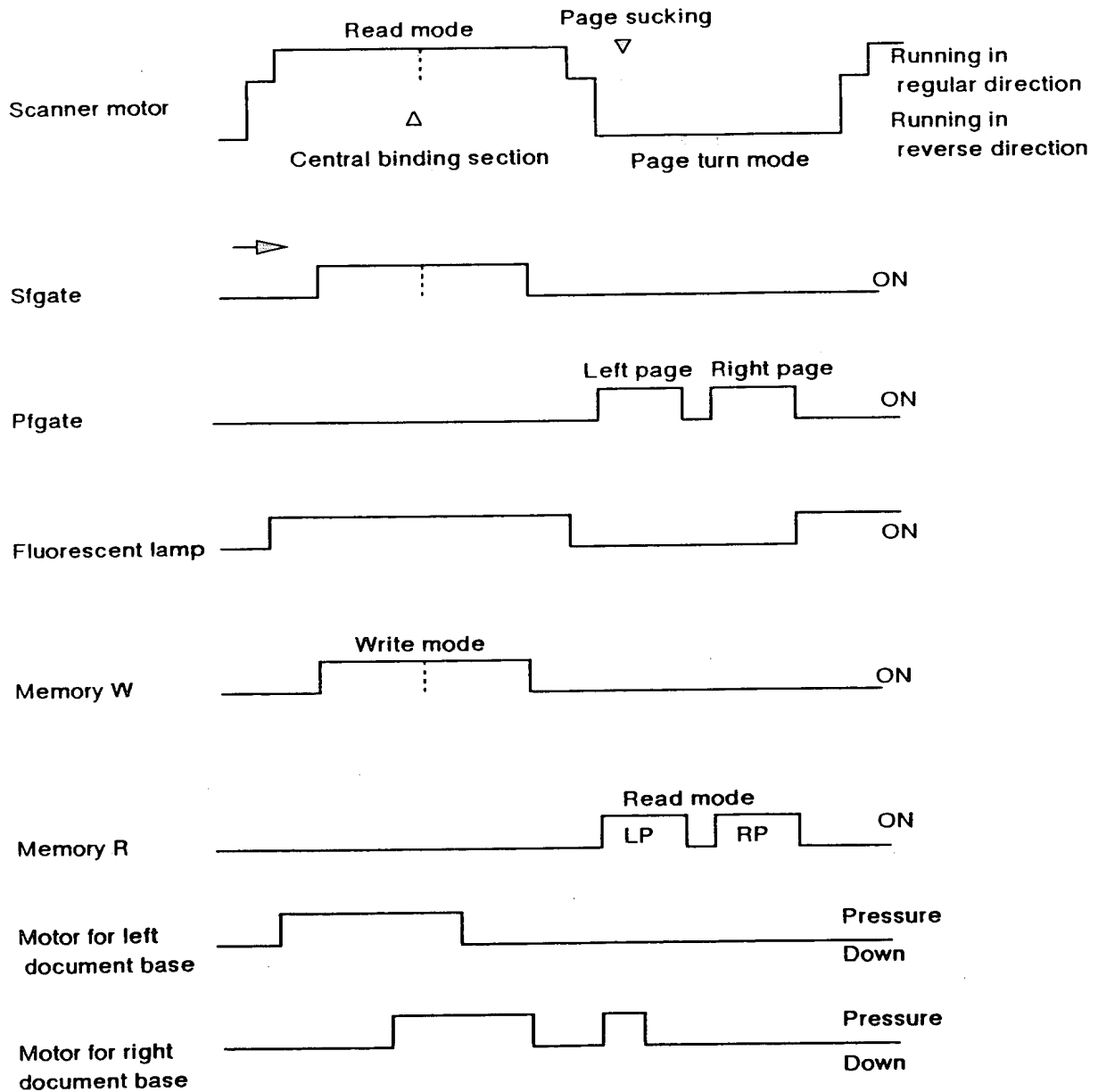


FIG. 61

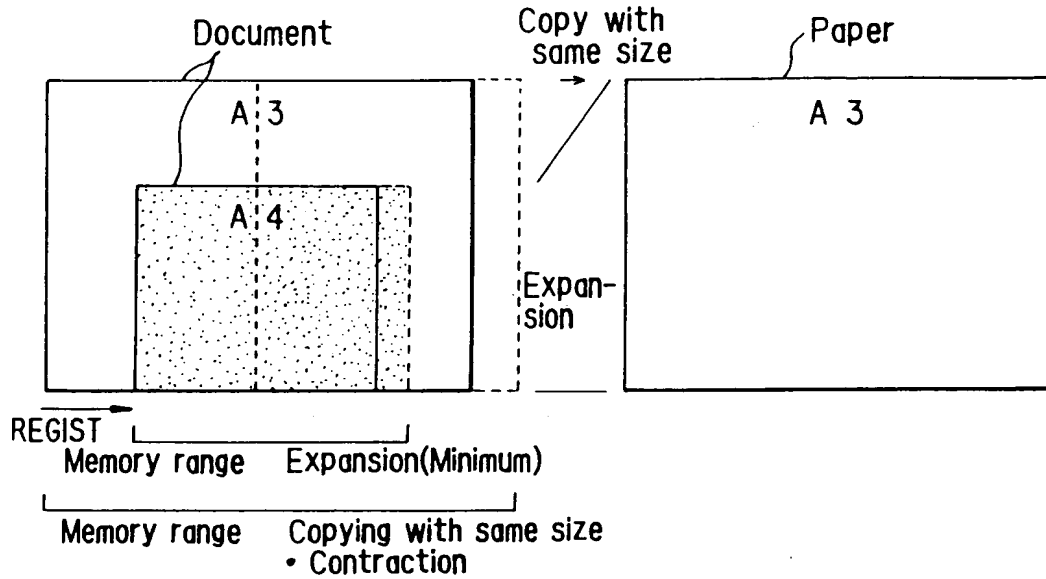
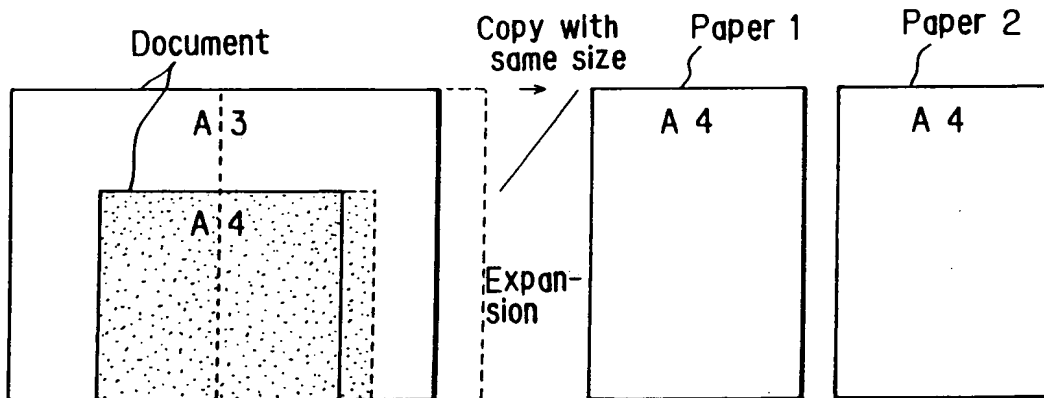
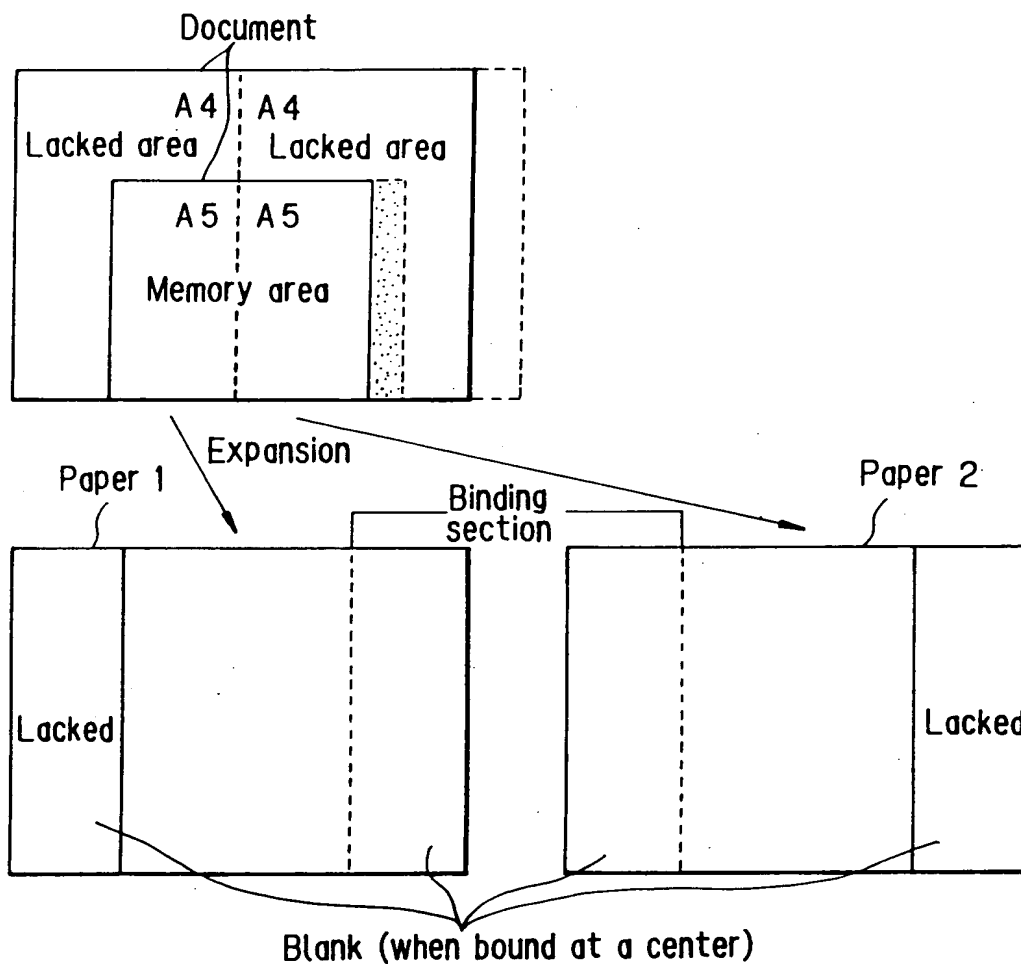


FIG. 62



201250 1169560

FIG. 63



095601-0910
T01260 T169560

FIG.64